

SCANNING PROBE ALLOYING NANOLITHOGRAPHY (SPAN)

A Dissertation

by

HYUNGOO LEE

Submitted to the Office of Graduate Studies of
Texas A&M University
in partial fulfillment of the requirements for the degree of

DOCTOR OF PHILOSOPHY

May 2009

Major Subject: Mechanical Engineering

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ABSTRACT

Scanning Probe Alloying Nanolithography (SPAN). (May 2009)

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In recent years, nanowires have become increasingly important due to their unique properties and applications. Thus, processes in the fabrication to nanostructures has come a focal point in research. In this research, a new method to fabricate nanowires has been developed. The new technique is called the Scanning Probe Alloying Nanolithography (SPAN). The SPAN was processed using an Atomic Force Microscope (AFM) in ambient environment. Firstly, an AFM probe was coated with gold (Au), and then slid on a silicon (Si) substrate. The contact-sliding motion generated a nanostructure on the substrate, instead of wear. Subsequently, careful examination was carried out at the scale relevant to an AFM probe, in terms of physical dimension and electrical conductivity. The measured conductivity value of the generated microstructures was found to be between the conductivity values of pure silicon and gold. Simple analysis indicated that the microstructures were formed due to frictional energy dispersed in the interface forming a bond to sustain mechanical wear. This research proves the feasibilities of tip-based nanomanufacturing. The SPAN process was developed to increase efficiency of the technique. This study also explored the possibility of the applications as a biosensor and a flexible device.

This dissertation contains nine sections. The first section introduces backgrounds necessary to understand the subject matter. It reviews current status of the nanofabrication technologies. The basic concepts of AFM are also provided. The second section discusses the motivation and goals in detail. The third section covers the new technology, scanning probe alloying nanolithography (SPAN) to fabricate nanostructures. The fourth talks about characterization of nanostructures. Subsequently, the characterized nanostructures and their mechanical, chemical, and electrical properties are discussed in the fifth section. In the sixth section, the new process to form a nanostructure is evaluated and its mechanism is discussed. The seventh section discusses the feasibility of the nanostructures to be used in biosensors and flexible devices. The conclusion of the research is summarized in the seventh section.

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NOMENCLATURE

AFM	Atomic force microscope
CD	The critical dimension (the smallest pattern)
DRAM	Dynamic random access memory
MEMS	Micro-Electro-Mechanical System
MLS	Motorized Linear Stage
MPU	Microprocessor unit
NEMS	Nano-Electro-Mechanical System
NSOM	Near-Field Scanning Optical Microscope
PVDF	Polyvinylidene difluoride
SEM	Scanning electron microscope
SPAN	Scanning probe alloying nanolithography
STM	Scanning tunneling microscope
TEM	Transmission electron microscope

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1. INTRODUCTION

This section provides background necessary to understand the motivation and objectives of the research. A review in lithography is provided. First of all, basic backgrounds and importance of lithography is discussed. The limitations of current lithography are explained and some alternative fabrication methods are discussed. Finally, the principles and fundamentals of Atomic force microscopy (AFM) which is a main instrument in this research are presented.

1.1. Lithography

1.1.1. Background

The word of lithography was from Greek. The ‘litho’ means stone, and ‘graphy’ writing; i.e., stone-writing. There are many lithography methods. The dominant patterning technique is the ‘photolithography.’ The ‘photo’ means light. This technique is the process to pattern the geometric shapes of mask onto silicon wafer. This technique has been used in semiconductor industry to make integrated circuits [1].

The entire process of the photolithography involves surface preparation (wafer cleaning), pre-baking, photoresist application (spin coating), soft baking, wafer loading, alignment, exposure, development, and hard baking. Figure 1.1 is a diagram to depict the process of the regular photolithography.

This dissertation follows the style of IEEE Transactions.

Ultraviolet (UV) light illuminates and passes through mask and lens, then exposes photoresist on a silicon wafer. The wafer is submersed into the developing solution [2]. The photolithography can be categorized into three parts with different space between the masks and substrates. The first is the mask contacted to photoresist samples. This is so called the contact printing. The advantage of the contact printing is that one can get higher resolution than other printing methods. The disadvantage of the contact printing is to damage the silicon wafer on the contact area [3-5]. Proximity printing method was invented for preventing the wafer damage due to the contact between the silicon wafer and the mask. This method is similar to the contact printing method. However, there is a small gap ($10 \sim 50 \mu m$) between the mask and the wafer silicon. Although this gap of the proximity printing minimizes the damage on the wafer, the resolution is not high, about $2 \sim 4 \mu m$ [6-8]. In order to increase the resolution and reduce the sample damage, the projection printing method was invented by Perkin-Elmer, 1973. This method removes the damage induced by direct contact completely and the resolution can be as small as $1 \mu m$. The disadvantage is that only a small area on the silicon wafer can be scanned. The process should be repeated for other areas until the entire area of the wafer is scanned. This method is called step-and-repeat system [9-13].

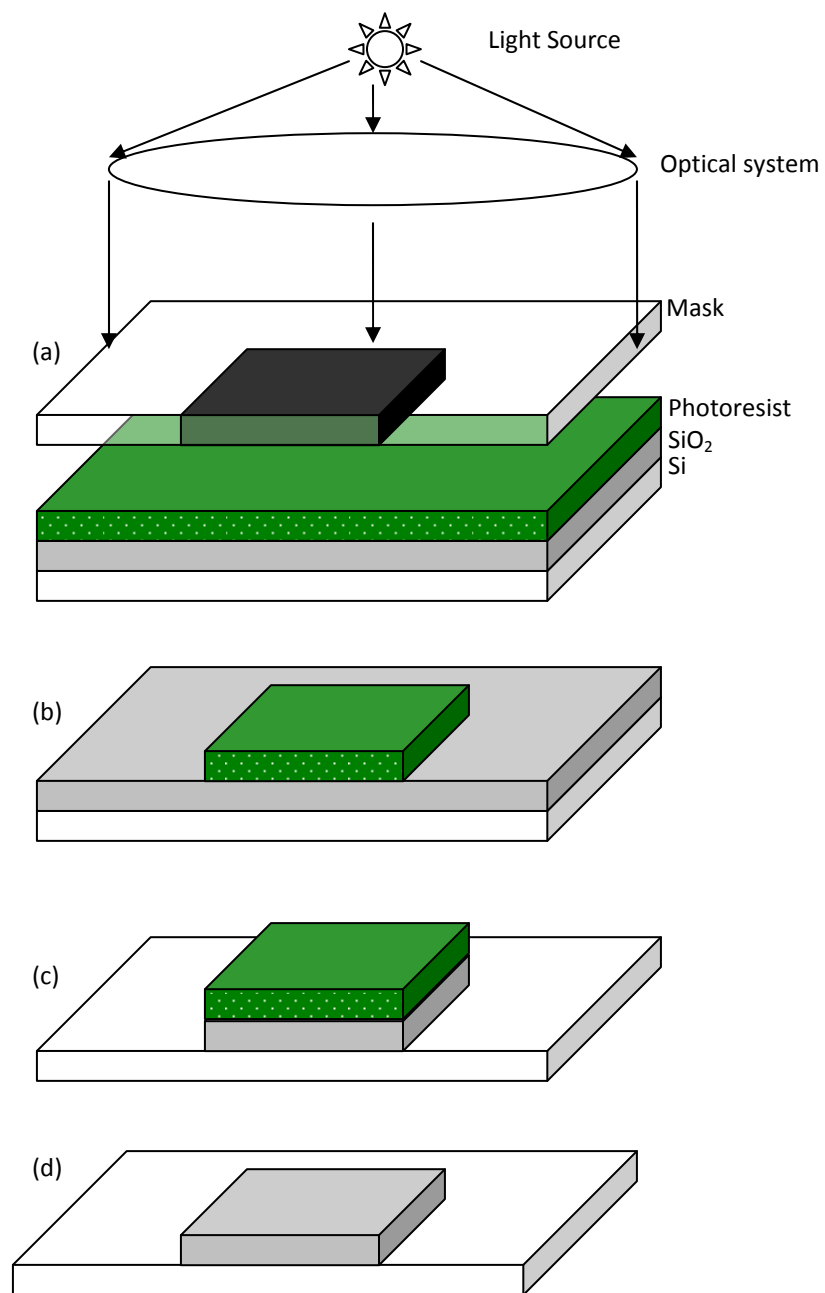


Figure 1.1 Steps of optical lithography process.

1.1.2. Importance of Lithography

Lithography plays a key role in semiconductor technology [14]. Lithography can enable or limit the growth speed of semiconductor industries. Table 1.1 lists the critical dimensions of a chip with years [14]. The lithography advanced IC manufacturing. As shown in Table 1.1, as the line dimensions are reduced, the number of transistors in a microprocessor unit increases, also the cost of one transistor decreases.

Table 1.1 The performance improvements of chips [14].

Year		1999	2002	2005	2008	2011
Dense Line CD (nm)		180	130	100	70	50
Isolated Line CD (nm)		140	100	70	50	35
M PU	Frequency(MHz)	600	800	1100	1400	1800
	Transistor/cm ²	6.2M	18M	39M	84M	180M
	Cost/Transistor (microcents)	1735	580	255	110	49
D RAM	Bits	1G	4G	16G	64G	256G
	Bits/cm ²	270M	770M	2.2B	6.1B	17B
	Cost/Bit (microcents)	40	15	5.3	1.9	0.66

CD: the critical dimension (the smallest pattern)

MPU: microprocessor unit

DRAM: dynamic random access memory

1.1.3. Limitations of Lithography

1.1.3.1. Resolution

There is a critical problem with the photolithography; resolution. The resolution is limited by optical diffraction. As shown in the following equations (Equ.1), the minimum printed-feature size (R , resolution) depends on the wavelength (λ) and the aperture (NA) of exposure system [15-23].

$$R = k_1 \frac{\lambda}{NA}, \quad DOF = k_2 \frac{\lambda}{NA^2}, \quad NA = n \sin(\theta) \quad \text{----- (Equ.1)}$$

where R is resolution, k_1 and k_2 are constants (normally 0.5-0.8 for k_1 and 0.1-0.5 for k_2) depending on the resist technology, λ is the wavelength of the transmitted light, NA is the numerical aperture (the sine of the convergence angle of the lens,) and DOF is the depth of focus.

To obtain smaller feature size, the wavelength λ should be decreased and the numerical aperture should increase. Thus, the wavelength has been reduced from 435nm (g-line [24-29]) to 405nm (h-line [30]) to 365nm (i-line [31]) to 248nm (DUV, deep ultraviolet [32]). The critical resolutions (CD) are $0.5 \mu m$, $0.4 \mu m$, $0.35 \mu m$, and $0.25 \mu m$, respectively. However, the depth of focus also will be decreased as increasing the resolution. The decreased depth of focus will reduce the process latitude (capability) of the optical system [23].

1.1.3.2. Alignment

One of other problems involved with lithography is the alignment [33-41]. For fabricating an integrated circuit, a number of layers should be printed. Typically 15 to 25

layers are demanded. Those layers should be precisely aligned for fabricating a non-defected chip. It is difficult to make a precise alignment for each layer. This leads to a trade-off between alignment accuracy and alignment time. In addition, there is a trade-off between resolution and alignment. As the pattern size decreases, it gets more difficult to make a good alignment between layers. Thus, alternative nanolithography methods were invented to solve the problems of above-mentioned lithography. One of the promising technologies is the AFM lithography [42-67].

1.1.4. Alternatives

There are several alternative lithographic technologies overcoming the resolution limits of optical lithography [68-74]. Extreme ultraviolet (EUV [23]) or X-ray lithography (XRL [75-77]) use smaller wavelength which is 13.4nm or 7Å, respectively. The small wavelength can be easily absorbed by air. The process of this lithography should be done in vacuum. The lenses and masks should be devised specially for this wavelength due to its absorption. Those reasons increase the process cost. Electron Beam Lithography (EBL [78-80]) can scan a finely focused beam and directly write features on a sample. The features can be as small as 10nm by using EBL. However, the throughput of EBL is very low. This means that the process is slower and more expensive than others. EBL is normally used for making masks of other lithography.

1.1.5. Scanning probe alloying nanolithography (SPAN)

There are many AFM based lithography methods [81-88]. In general, the methods can be categorized into two groups; force-based lithography or bias-based one. In bias-

based lithography, the AFM tips are biased to create an electrical field between the tips and the substrate surfaces. For force-based lithography, larger forces are loaded on the substrate surface, which fabricate a structure on the surface. In the following, SPAN of bias-based lithography will be discussed. The SPAN is a new technology of among force-based lithography methods. The SPAN has significant advantages over the regular lithography. With AFM lithography, the patterns can be fabricated on various materials such as metals, semiconductors, and polymers. SPAN can be processed in air, liquid, or vacuum environments. The fabricated patterns by SPAN can be characterized by using AFM immediately.

1.2. Introduction of AFM

Atomic Force Microscope (AFM) is being used in wide range technologies, such as biological, chemical, and electrical industries. In addition, AFM is being applied to fabricate nano-materials or devices. AFM is a type of Scanning Probe Microscope (SPM). The SPM covers technologies to image and measure surfaces on a micro- or nano-scale. There are three most common types of SPM; Scanning Tunneling Microscope (STM), Atomic Force Microscope (AFM), and Near-Field Scanning Optical Microscope (NSOM). These three types share the concept that an extremely sharp tip (3-50 nm radius of curvature) scans a substrate surface. In such the system detects the surface properties, such as topography, phase, or conductivity.

While AFM measures the interaction force between the tip and surface, the STM measures a weak electrical current flowing between tip and sample, and NSOM detects light energy very close to the sample.

1.2.1. Comparison of AFMs

The STM is the origin of an AFM. Sometimes, STM resolution is better than AFM because tunneling current of STM dependent exponentially on distance between the tip and the substrate surface. But, the samples of STM should be conductive while AFM can be used for both of conductive and non-conductive samples. That is, the AFM is more versatile than STM. Scanning Electron Microscope (SEM) has good depth of field, and scans very fast. However, it needs samples coated with conductive materials such as Ag, Au, and Au-Pd. While SEM can get a 2D image, AFM shows 3D images. AFM doesn't need vacuum system which SEM needs. The images of AFM have good contrast even the samples are not coated with conductive materials. Compared with Transmission Electron Microscopes (TEM), the samples of AFM don't need special and expensive preparation. Also, AFM gives more complete information than the two dimensional profiles available from cross-sectioned samples.

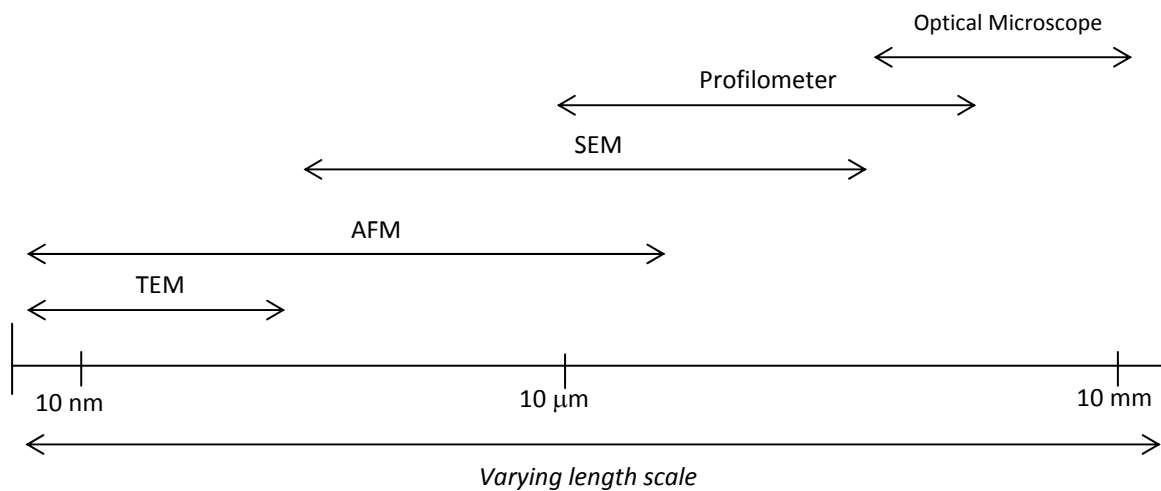


Figure 1.2. Comparison of AFM with other measurements (Courtesy of PNI).

As shown in Figure 1.2, the scanning range of AFM is broader than others. TEM can be used to get sample information at a few nanometer or micrometer ranges which AFM can cover. Profilometer cannot give 3D information, but only cross sectional information of samples.

1.2.2. History

Binnig, Rohrer, Gerber, and Weibel introduced STM in 1981 [42, 89, 90]. The STM opened the imaging technology of atom scale. Figure 1.3 shows the conception of the STM that a sample surface could be interpreted from its deformations. After one year (1982) of its introduction, the adatom layer of Si (111) was imaged with an STM by Binnig et al.[42, 89, 90] (See Figure 1.4).

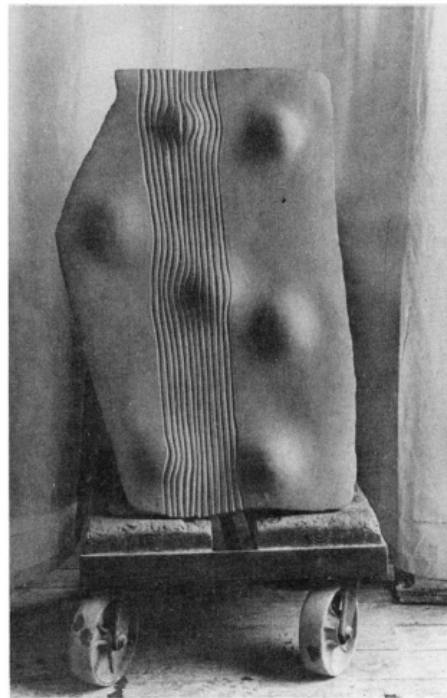


Figure 1.3. The conception of STM. (sculptor, Ruedi Rempfler)

G. Binnig and H. Rohrer were rewarded with the Nobel Prize in Physics in 1986. The STM is a milestone of the nanotechnologies, but it has a serious limitation. That is, STM use the tunneling current flowing between a tip and a sample so that the sample should be an electrical conductive material.

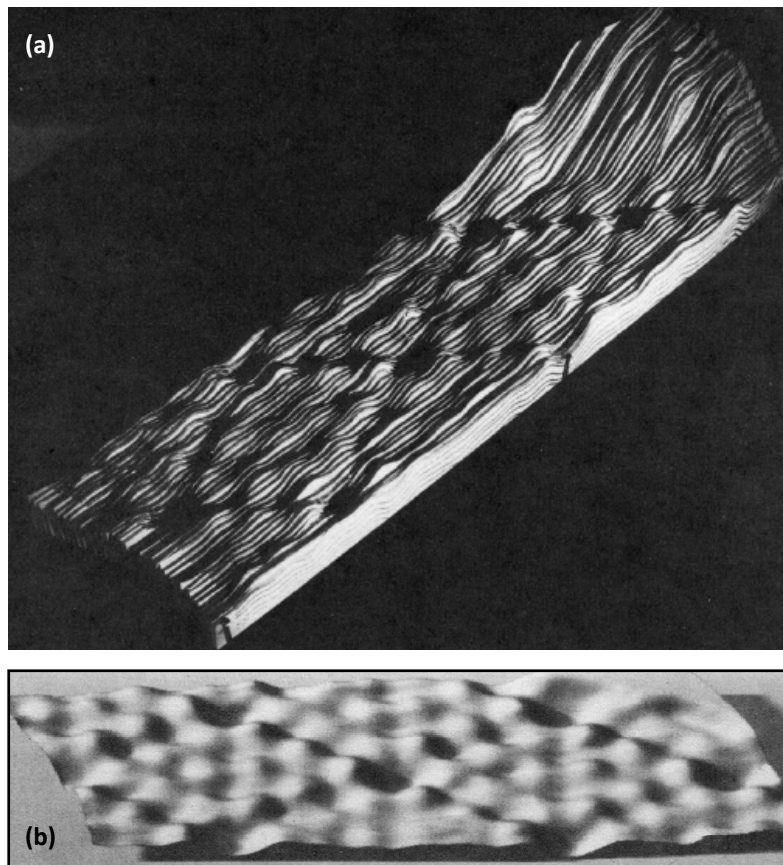


Figure 1.4. The first scan images of STM by Binnig et al. in 1983; (a) the original tracing image (b) the processed image of 7×7 reconstruction of Si (111).

From the experiments of STM, Binnig found that there were significant forces between the STM tip and the sample when the distance of the tip and sample was enough

close to flow the tunneling current [89]. By using the forces, Binnig et al. invented AFM [42, 89, 90]. In the result, the first commercial AFM was fabricated by the company, Park Scientific. Consequently, thousands of AFM's are in use in university, public and industrial research laboratories all over the world.

1.2.3. Laser and Detector

There are forces between an AFM probe and a sample when the probe contact to the sample surface; attraction and repulsion forces. AFM operate with the forces [89]. As the tip drags over the sample, the forces between the tip and the sample have the cantilever of the tip deflected or twisted. A laser beam of AFM is reflected and detected by a photo-detector of AFM. The reflection direction of the beam is changed due to the cantilever deflection. The detector measures the position of the reflected beam on the detector horizontally and vertically. The schematic of the laser beam, the photo-detector, and the cantilever is shown in Figure 1.5. The detected signals are processed by the AFM computer (controller). The vertical change of the reflected beam position on the detector indicates the sample height. The horizontal change is for the frictional force between the probe and the sample. The differential amplifier in Figure 1.5 enlarges the amplitude of the output signals of the photodetector.

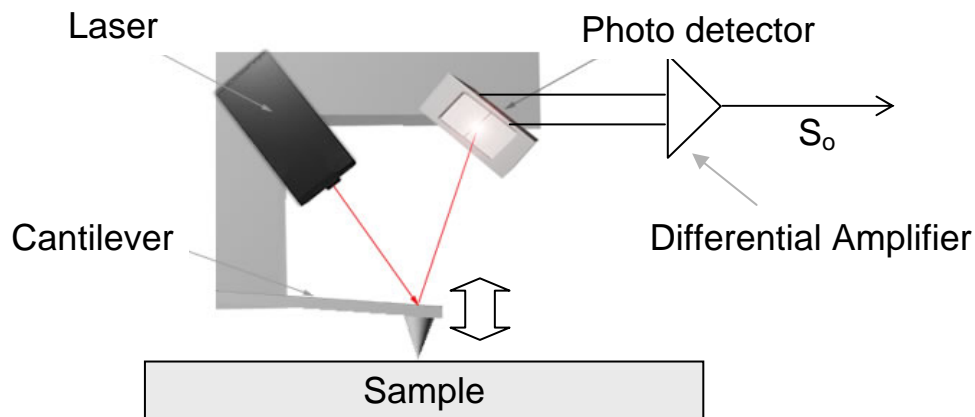


Figure 1.5. Schematic of the structure of AFM with Laser, photo detector, and cantilever. (Courtesy of PNI)

1.2.4. AFM system

Applying electrical energy to a piezoelectric ceramic material creates the mechanical movement, which called an electromechanical transducer [91-94]. The transducer is mostly used in AFM system. A typical piezoelectric disc will vertically expand by about 1nm per applied volt. It's possible to expand a piezoelectric disc up to 0.1mm by applying 100V. The larger motions can be made by adding more layers of piezoelectric materials on the piezoelectric disc. That is, the piezoelectric discs including 1000 disc layers expand 1000nm when exposed to only 1V energy potential. Figure 1.6 shows an example that the expansion of a piezoelectric disc is achieved when they are exposed to an electric potential. The motion size, speed, direction depends on the type of piezoelectric material, shape, the number of the added discs, and the applied electric energy strength.

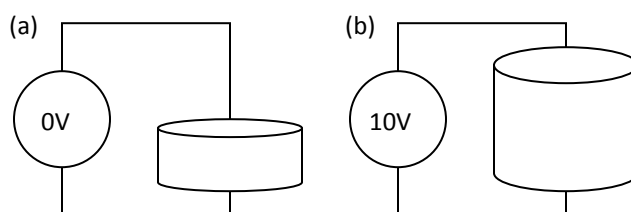


Figure 1.6. Piezoelectric Ceramic disc. (a) Applying no voltage will not make any change on the disc, but (b) applying some voltage will vertically expand the disc at the rate with 1nm per 1 volt.

Those piezoelectric ceramic discs are used for x, y, and z transducer controllers [95]. As shown in Figure 1.7, there are X, Y and Z motors. By using the motors, the AFM probe is roughly placed the location close to the sample where will be imaged. The z piezoelectric ceramic transducer places the probe to the surface very precisely. During scanning, x, y and z piezoelectric ceramic discs are used because their motions in nanometer amount can be controlled.

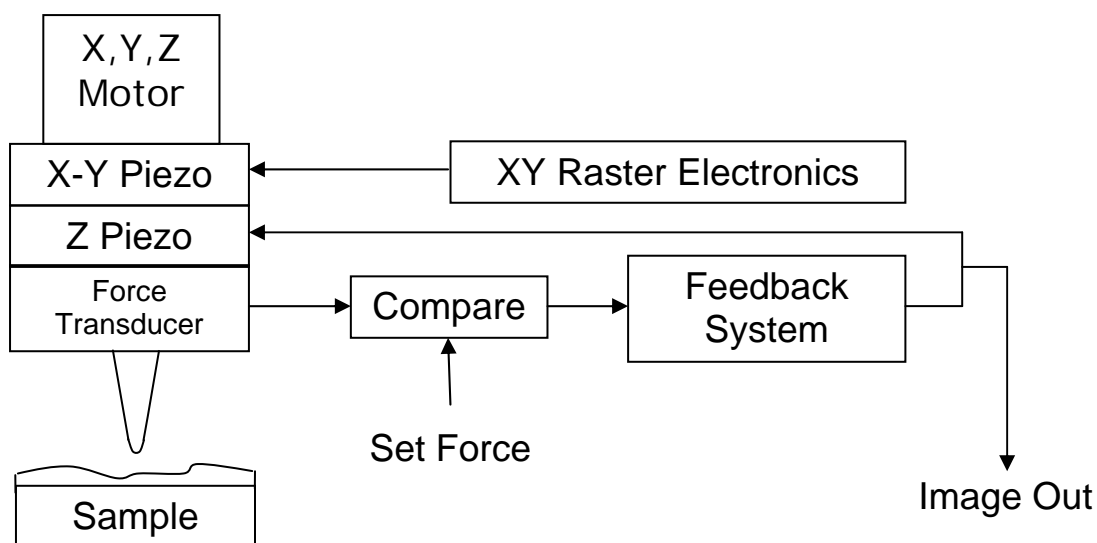


Figure 1.7. The system diagram of an Atomic Force Microscope. (Courtesy of PNI)

The piezoelectric discs for x and y are expanded while the probe is moved on the sample surface. The feedback system takes the signal from the force sensor. Then, the signal generator of the feedback system generates output voltages. The voltages control the Z-piezoelectric ceramic to change the Z-height of the probe. This voltage is necessary to keep the cantilever deflection constant while scanning.

1.2.5. Probe Approach

There are many mechanisms for the probe approaching to a sample surface [89, 96-99]. To bring the probe from 1mm above the surface to within 0.1nm in 60 seconds is same to fly to the moon in 60 seconds and stop within 38 meters of the surface. This would require traveling at 23 million kilometers per hour. A typically used approach method is ‘Wood Pecker’ tip approach. The Z motor goes down $1\mu m$, and then the z piezoelectric ceramic disc have the probe cantilever down to $5\mu m$. While the z ceramic disc make the cantilever go down, the force sensor system monitors if the probe is close to the sample surface. If the probe is still far from the surface, the Z motor goes down more $1\mu m$. Then, the z ceramic disc scans again. The system repeat these step until the probe gets close to the surface. This mechanism is called as ‘Wood Pecker’ tip approach because it’s very similar to the behavior of the bird, wood pecker.

1.2.6. Cantilever and Probe

There are many kinds of cantilevers and probes [56, 100] as shown in Figure 1.8. Those are categorized by their shapes, materials, and usages. Typically, cantilevers are either triangular or rectangular shaped. The cantilever shape and materials affect on its

spring constant value. That is, the triangle shaped cantilever has higher spring constant value than that of rectangle shaped one. Also, it can stand higher torsion or higher vertical-bending force. The rectangle cantilever can be used to scan softer samples. The probe materials can be diamond, silicon or silicon nitride. Probes shapes can be pyramidal or conical and may be sharpened.

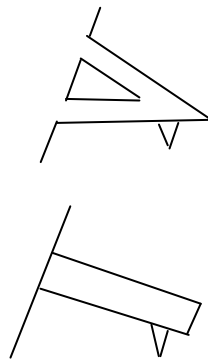


Figure 1.8. Two kinds of cantilevers; triangle and rectangle shapes. (Courtesy of PNI)

1.2.7. Mode

The AFM become much more versatile and capable than before various modes were developed. Two modes can be broadly categorized; DC and AC modes. Those two modes are separated with oscillation of AFM cantilevers. That is, AC modes oscillate the AFM cantilevers, but DC modes don't. The contact mode of AFM doesn't use the oscillation. The contact mode is a common mode used in AFM to scan a sample surface with the AFM probes. Although the term 'contact' is used for the mode, actually the tip doesn't touch the sample because of the repulsive force between the probe and the sample. (See Figure 1.9) The cantilever is pushed against the repulsive force from the surface to the

probe. The deflection of the probe cantilever is detected by the force sensor elements. If the contact mode probe scans on a sample surface, the electrostatic charge will be trapped.

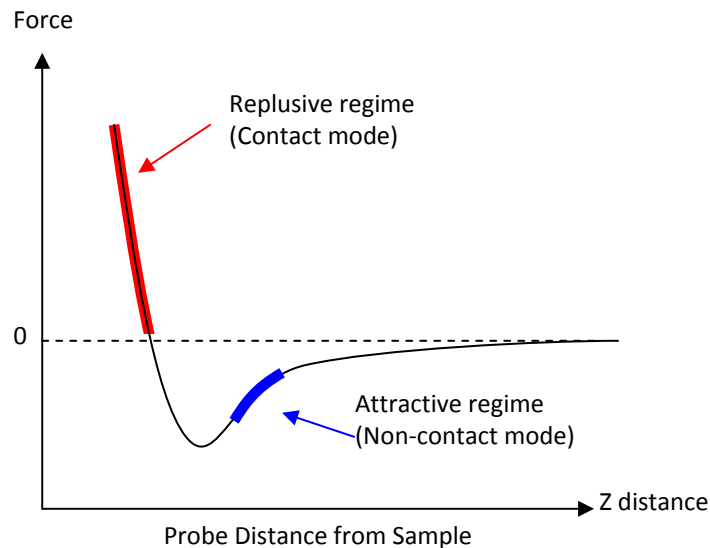


Figure 1.9. Potential energy (force) diagram of the interaction between a probe from a sample. The red line is for repulsive force, and the blue one is attractive regime. (<http://www.mobot.org/jwccross/spm/>)

This charge substantially causes additional attractive forces between the probe and the surface. Those additional attractive forces effect on the cantilever or sample. The cantilever will be twisted. Sometimes, this twisted cantilever is monitored, and used for imaging the frictional image of the sample surface. However, the forces which are enough to twist the cantilever will damage on soft samples such as biological or chemical samples. Also, the forces can dull the cantilever probe or distort the scanned images. Thus, there is a solution of those forces causing damages on samples. The solution is the

non-contact mode. While the contact mode AFM is in the repulsive regime of the potential energy diagram of the probe-sample interaction, the non-contact mode is in the attractive regime as shown in Figure 1.8. In non-contact mode, the probe doesn't touch the sample at all. The probe is always scanned 50~150Å above the surface. Van der Waals forces between the probe and the sample surface are detected to image the surface [101]. However, the Van der Waals forces are much weaker than the forces of the contact mode. The probe should have an oscillation. This method is called AC detection. The oscillation is used to detect the forces between the probe and the sample surface. The cantilever will be also affected by the force gradients from the sample. Finally, the AFM measures the changes of amplitude, phase, or frequency of the cantilever oscillation. Another solution for the friction forces between the probe and the sample is the tapping (intermittent contact) mode of AFM. This mode overcomes the problems related to friction, adhesion, and electrostatic forces. It also doesn't damage the soft samples while scanning [102]. The tip in the tapping mode provides higher resolution than other modes, but in practical it's more difficult to get images by using the tapping mode tip [103]. While scanning the surface, the tip drags over the sample and oscillates simultaneously [53]. The z piezoelectric disc drives the cantilever oscillation with high amplitude of more than 20nm. The frequency of the tapping tip oscillation is about 100 kHz and 400 kHz with vibration amplitude of a few nanometers. While non-contact mode imaging generally provides low resolution, tapping mode achieve high resolution without destructive frictional force. That is, the tapping mode tip can be used for the very soft and fragile samples successfully.

1.2.8. Resolution

AFM has two resolutions; the plane (x, y) resolution and vertical (z) resolution [104]. The x and y piezoelectric discs are used to scan the probe on a sample. The plane resolution means how precise the sample surface can be imaged. The resolution mostly depends on the sharpness of the probes [105-108]. Thus, many methods to fabricate the probes are investigated [52, 56, 100, 109-114]. The recent AFM technology established a resolution of 10pm.

The vertical resolution is for the perpendicular direction to the sample surface. The height changes of the probe are monitored. Those changes give information to the imaging processor to get an image for the surface height. However, some noises decrease the vertical resolution. The noises are from vibrations of the cantilever. The vibrations should be minimized to obtain the maximum vertical resolution. The source of the vibrations typically is from acoustic noise, floor vibrations, or thermal vibrations.

1.2.9. Measuring Properties

AFM is very powerful to measure most of all properties of a sample, such as mechanical, thermal, electrical, and magnetic properties. For the mechanical properties of a sample, AFM can measure the force-distance curve, friction (lateral) force between the tip and the sample, and phase changes. Phase imaging is an extension of tapping mode of AFM. The phase image provides nanometer-scale information of a sample surface. The oscillation the tapping mode tip changes when the tip meets other materials as the tip scans the sample surface. The changes of the oscillation consist of the phase as well as the amplitude of the oscillation. The different materials on the sample cause phase lags of the cantilever oscillation. The phase lag is sensitive to adhesion, viscoelasticity, or friction. The phase images can be used to identify sample contaminants and different components. It also can differentiate the surface area having high or low adhesion forces or hardness. Another mechanical property of a sample measured by AFM is force-distance curve.

1.3. Nanowires

In recent years, nanowires have attracted tremendous interests due to their unique properties. The diameter of a nanowire is about 50 nanometers or less. It can be categorized into two types: sub-microstructures ($1\mu\text{m}$ – 100nm) and nano-structures (1 – 100nm). At the nano-meter length scale, there is the confinement of charged carriers in their own structures such as nano-films, nano-wires, or nano-dots, as shown in Figure 1.10.

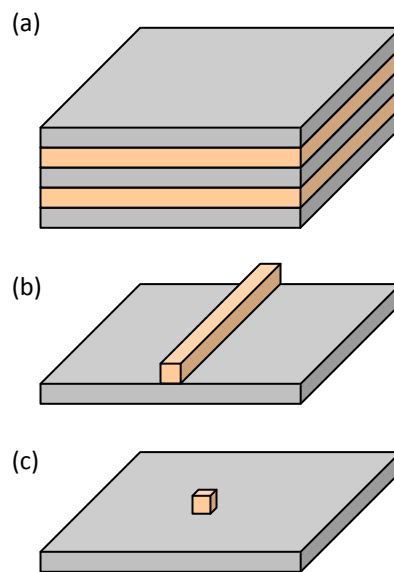


Figure 1.10. Three nanostructures. (a) quantum well, (b) quantum wire, and (c) quantum dot.

The confinement drives important quantum mechanical phenomena of nanowires. The conductance is quantized, the band-gap is modified, and coulomb is blockaded [115]. Those phenomena will be utilized for its applications such as a single-electron

transistor which operates at a low power. The transistor works as the base for nanowire devices. There are many different types of nanowires. They can be broadly separated into three types; metals, semiconductor, and insulators. Metallic nanowires include Ni, Pt, or Au. Semiconducting wires are Si, InP, or GaN. The nanowires composed of SiO₂ or TiO₂ are insulating. These nanowires are inorganic. Organic materials, such as DNA, are building the nanowires by repeating molecular units.

1.3.1. Properties

Due to their nanometer-length-scale size, nanowires have peculiar electrical, mechanical, and optical properties. Electrical properties are a key for being used in semiconductor. The properties can be applied to semiconductors or sensors. Current flowing throughout the nanowires is changing with small change of applied electrical potential due to the edge effects [116]. There are many electrons on a nanowire surface that are not bonded. Those free electrons on the surface play a source of conductivity of nanowires. This effect is called an edge effect [117]. As decreasing the size of nanowires, the edge effect becomes more significant to the nanowires' properties.

1.3.2. Applications

Nanostructures can be used in electronic or optical devices and sensors. In the nano-sized electronic device areas, most researchers are interested in nanometer-sized transistors to extend Moore's law beyond 100 nm resolution [118]. It has been known that nanowire transistors are at least four times faster than conventional semi-

conductive devices [119]. The devices are inexpensive, but save space so that they can be used in displays or cell phones. In optical applications, there are optical sources and detectors made with nanowires. The nanowires in the applications improve their own characteristics which cannot be achieved by large scale devices. The nano devices have lower threshold current so that they can save energy and perform faster and more dynamically than bulk-wire optical devices. In addition, they can improve the emission line-width in quantum dot lasers. In the sensor application area, the nanowires behave sensitively and selectively more than conventional optical lithography does. Cui et al.[120] devised a sensitive electrical sensor. The sensors perform in real-time as a chemical or a biological sensor. The researchers fabricated doped-silicon nanowires for the sensors. As changing the pH values in a solution, the conductance of the nanowire sensors will be changed nonlinearly. Thus the sensors detect pH values with high sensitivity [121].

1.3.3. Fabrication of Nanowires

Researchers have attempted to fabricate the nanowires in several ways [122]. Nanofabrication focuses on technologies to fabricate sub-micron or nano-scale structures. To achieve nanofabrication, the optical lithograph has been developed continuously. However, there is a limitation of the lithography method; trading-off between resolution and accuracy. The resolution is for minimum resolvable feature, and the accuracy is from Depth of focus (DOF) [123]. Rayleigh's equations have been used to optimize the typical optical lithography to obtain a high resolution with accuracy [123]. According to Equ.1, the resolution R trades off with the depth of

focus (DOF). However, the depth of focus becomes small so that the exposure process becomes sensitive to the distance between the substrates and lenses, and the position of the resist layer [124]. To improve the resolution, various methods have been invented. Methods can be broadly divide into top-down and bottom-up categories. The top-down approach starts with bulk materials and removes the materials in a specific area to fabricate nanostructures. The top-down approach is an extension of the typical optical lithography or micromachining. This approach is close to the mechanical or optical engineering. On the other hand, the bottom-up method builds or grows a structure on a substrate. Over the past few years, the bottom-up approach is mainly focused on the synthesis of low-dimensional semiconductor materials. Hence, the bottom-up approach is closer to chemical engineering or material science than the top-down approach. For the top-down techniques, there is Electron-beam lithography (EBL) to pattern nanowires [125, 126]. EBL has a disadvantage of which throughput is very low. To overcome the low throughput of EBL, the nanoimprint technology is invented [127], but it makes the fabrication complicated. There are three main methods in bottom-up fabrication; epitaxy engineering [128-133], self-assembly manufacturing [134], and template-assisted synthesis [135-137]. However, for those methods, it's difficult and complicate to locate a nanowire on a specific area.

1.4. Needs

As predicted by the Moore's Law, the device dimensions of production transistors have been decreased exponentially since 1965 [138]. In 1998 the device

dimensions faced a wall of 250nm that it was difficult for the typical conventional lithography techniques to break through [138]. In 1997, the Semiconductor Industry Association predicted that critical dimensions would decrease to 150nm by 2001, to 100nm by 2006, and to 50nm by 2012 [139]. The current size is about 50nm To date, the conventional optical lithography used to fabricate the integrated circuits is facing the limitations of resolution-accuracy trading-off, as discussed in the introduction section. An alternative lithography may be devised within the next five years to achieve and extend Moore's Law. In such the scanning probe alloying lithography will be investigated to pattern 100nm feature sizes and below.

Both of bottom-up and top-down methods to fabricate the nanowires have limitations. The top-down techniques to fabricate the nanowires have been the fundamental method of semiconductor devices [118]. Nevertheless, as explained in the section, 1.4, the methods cannot obtain high accuracy and small enough features to be used in nano-devices. Among the top-down methods, EBL is well established for high resolution with high accuracy. This process is also very slow and expensive. On the other hand, the bottom-up methods can manufacture nano-sized features. The down side is that they are expensive and systems are complex to operate. It's mostly impossible to locate the nanowires in a specific region during the process [140].

2. OBJECTIVES

The long term objective of this research is to develop new nanomanufacturing processes in order to fabricate nanostructures with unique properties. There are four objectives in this thesis research:

- A. Develop a nanomanufacturing process, Scanning probe alloying nanolithography (SPAN).
- B. Process optimization.
- C. Develop a methodology to characterize nanostructures.
- D. Obtain basic understanding of the interfacial phenomena.

- A. Develop a nanomanufacturing process, SPAN.

We will develop the SPAN technology using an AFM for its high resolution and ease in control. In the present research, a new method to fabricate nanowires will be developed. The method is called the scanning probe alloying nanolithography (SPAN). The SPAN is similar to write with a pencil on a piece of paper. The alloying is simply referring to the nature of mixer of the Au-Si that dominates the property of nanowires. This process is different from any other AFM based methods. Comparing with the dip-pen lithography, this process is simple and doesn't use metallic ink. This process can use electrical potentials however being different from AFM Oxidation lithography. The latter applies only electrical potentials without a mechanical force in order to form a

nanostructure. The major difficulties of SPAN are to increase productivity and efficiency of the process. We will overcome them by controlling the temperature of substrate surfaces. Another difficulty of the SPAN is to obtain uniform nanostructures. For this issue, the contact-sliding direction is discussed. It is expected that this process would enable nanostructures to be located in a desired area and be capable to fabricate nanostructures at 100nm sizes or smaller. The efficiency and precision of the SPAN process was expected to be higher and better than existing techniques. In addition, the materials used in SPAN would be wider than other techniques. Furthermore, the SPAN is able to overcome the limitations of the conventional nanofabrication methods as mentioned above. With the capability of SPAN, nanostructures were fabricated.

B. Process Optimization.

The process of SPAN will be optimized to increase productivity, efficiency, and uniformity of the nanostructures by evaluating the process parameters. There are four main parameters; probe sliding speed, down-force of the probe, sample surface temperature, and the number of strokes. It's critical to understand the complete process of SPAN for developing and optimizing the process to fabricate a nanostructure efficiently.

C. Characterization.

In this objective, nanostructures generated by SPAN will be characterized. To understand the surface properties, behaviors, and structures of the nanostructures, the nanowires will be characterized for physical, chemical, electrical, and mechanical

properties. We will use different high resolution techniques, such as AFM, TEM, SEM, EDS, and nanoindentation, among others.

D. Interfacial phenomena.

Under this objective, the interfacial phenomena in SPAN occurred during the process will be studied. Understanding the phenomena is needed to develop and optimize the SPAN process. Study the mechanism of SPAN; how the nanostructures are formed by contact-sliding, materials transfer, and the interface between nanostructures and the substrate will be investigated.

3. PROCESS DEVELOPMENT OF SCANNING PROBE ALLOYING NANOLITHOGRAPHY

In this research, Scanning Probe Alloying Nanolithography (SPAN) is developed. The experimental setup is sketched in Figure 3.1. The AFM probe approaches on a substrate and writes a nanostructure. It's similar to pencil writing on a paper. The 'writing' process experiences friction between the sharp probe and the substrate, resulting in occurring alloy of the materials at nano-scale. The process involves thermal, physical, and chemical principles, such as friction and wear.

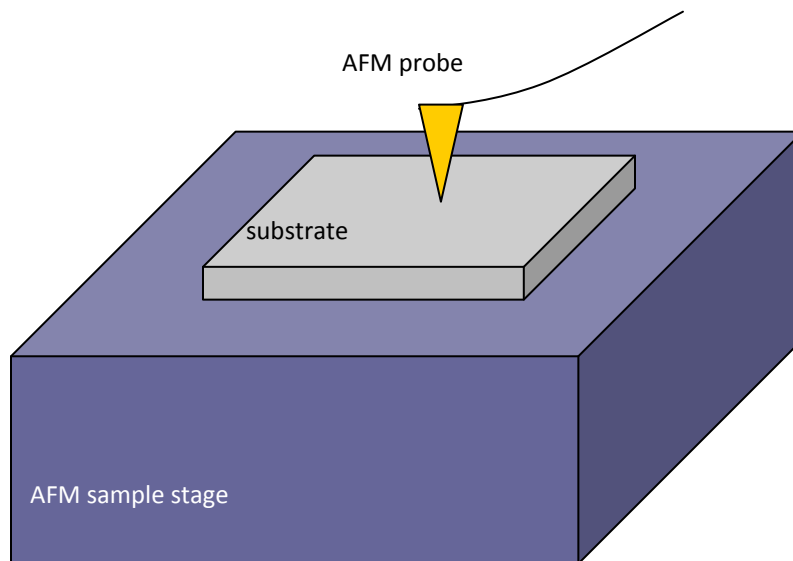


Figure 3.1 Simple diagram of experimental setup with AFM probe and a substrate. The AFM probe is slid on the substrate to 'write' a nanostructure.

3.1. Contact-sliding Experiments

Contact-sliding experiments were conducted by using an AFM (Nano-R, Pacific Nanotechnology Inc.) in a contact mode. With the down force of 503nN of Au-coated AFM probe, the Au tip slides against Si at the given speed (480 μ m/s). The applied load and contact-sliding speed were kept constant during tests. The gold was coated using a thermal evaporator (Edwards, Model: Auto 3063). Chromium (Cr) was deposited on a Si₃N₄ AFM tip to serve as an adhesion layer. Gold with 99.999% purity was deposited subsequently. The thickness of Cr and Au coatings were 15nm and 250nm, respectively. The surface characterization after contact-sliding experiments was conducted by using the same AFM with a standard Si₃N₄ tip. Close-contact-mode, also called as tapping-mode, was applied for this purpose with the scan rate at 1Hz for a 5 μ m in length.

The gold-coated probe was slid against the Si substrate in the contact mode. Figure 3.2 shows the schematic diagram of the experimental setup. The location, down-force, and sliding length of the probe were precisely controlled through the control panel of the AFM. Nanopatterns could be formed on the substrates by repetitive sliding of the probe. The shapes and dimensions of the patterns could be controlled by adjusting the down-force, sliding speed, temperature, and the number of strokes. Effects of electrical potential on the nanostructure were also investigated by applying a potential directly to the probe and substrate.

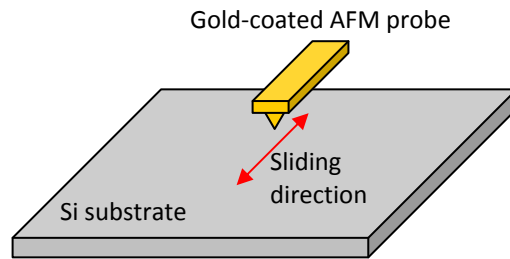


Figure 3.2. . The gold-coated AFM probe was slid on Si substrate along the red-colored direction.

3.2. Materials

For this study, the AFM is used as the major research instrument. Gold (Au) is coated on an AFM probe which works as a pencil. The substrate for this research will be Silicon (Si).

3.2.1. AFM

There are several advantages using an AFM. It is operated in normal ambient environment without a vacuum system which is needed in STM-based techniques. It has good reproducibility. The SPAN technique is expected to work with a wide range of materials. The operation and concept of an AFM is not complicated, but the control is very precise and simple. Figure 3.3 shows the AFM probe stage and the optical microscope. Using the optical microscope makes its operation easier and precise. Photodetector is sensitive enough to read nano-scale size features.

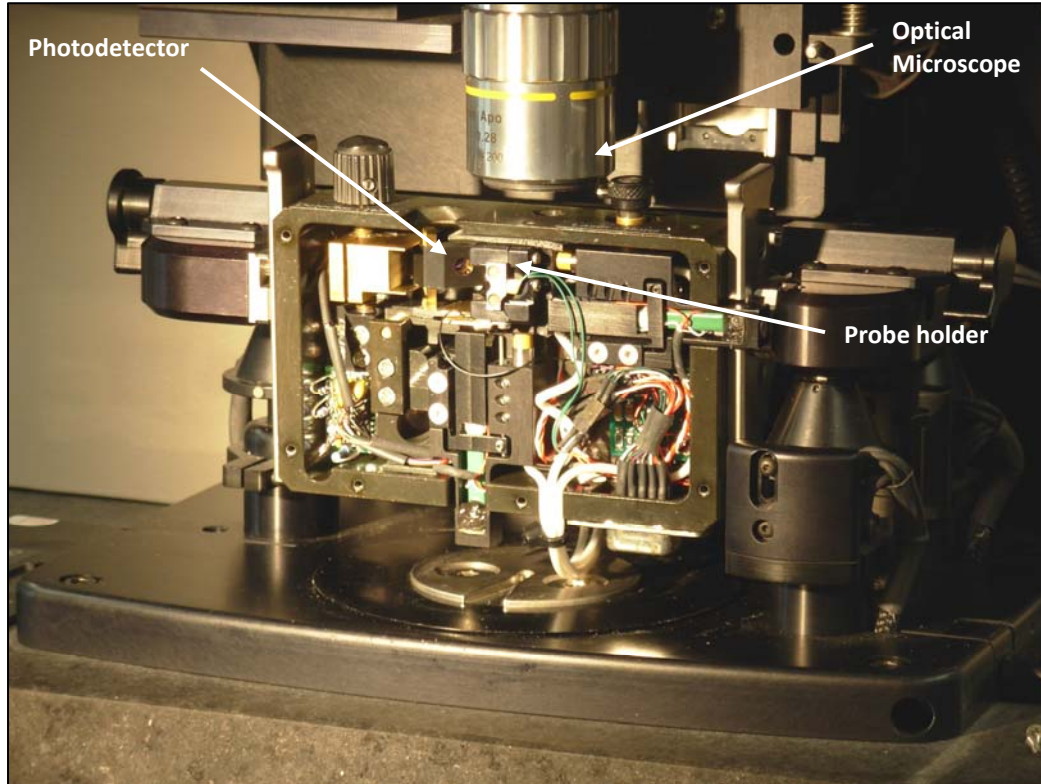


Figure 3.3 AFM probe stage and its circuits. AFM laser beam and the photo detector are embedded. It includes an optical microscope to operate easier.

3.2.2. Au & Si

For further applications of the nanowire into semiconductor devices, silicon (Si, 100) has been chosen as the substrate for this research. To fabricate a conductive nanowire, a highly-conductive material such as silver or gold should be chosen. Silver (Ag) has the lowest electrical resistivity ($15.87\text{n}\Omega\text{m}$) which is lower than gold (Au, $22.14\text{n}\Omega\text{m}$). The melting point (962°C) of Ag is lower than that of gold (1064°C), shown in Figure 3.4. Despite of the advantages of Ag, gold has been chosen for this study.

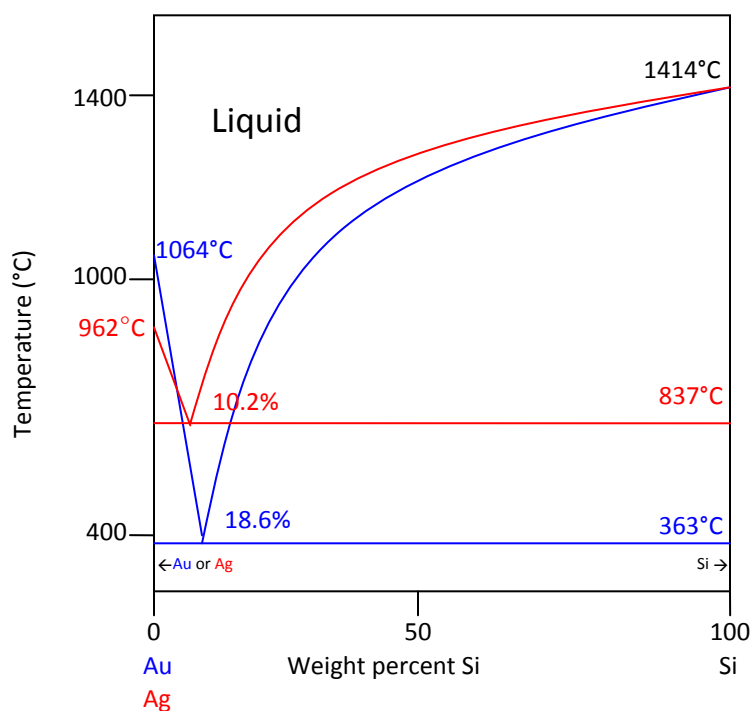


Figure 3.4 Phase diagram of Au and Si. The x-axis is for the weight percent of Si. The y-axis is the temperature. The blue-colored is for Au with Si, and the red is for Ag with Si.

The conductive nanowires will be fabricated with Si which drives the nanowires to an alloyed material. In the alloy materials, the eutectic points of them are more important than the melting points of each material involved in the process. The eutectic point of gold with silicon is lower than Ag with Si. The temperature of eutectic point of Au with Si is 363°C which is almost three times less than Ag with Si (867°C).

The gold material was chosen due to its stable properties, high conductivity ($45.2 \times 10^6 \text{ S} \cdot \text{m}^{-1}$), low resistivity ($2.44 \times 10^{-8} \Omega \cdot \text{m}$), and low eutectic temperature (360°C) with silicon. Au has an elastic modulus of 78GPa, hardness of 20Hv, and its crystal structure is cubic face centered (FCC). For further applications of the nanostructures

formed by SPAN as a biosensor, gold is suitable due to its strong adhesion with a thiol group [141, 142]. Single crystal silicon (Si, 100) was used as a substrate. Silicon is one of the most common materials in the semiconductor industry. This material has also been used in MEMS and other electronic devices [143-146]. The melting point of silicon is 1420°C. Its crystal structure is a diamond cubic and the Young's modulus is 150GPa. For each experiment, the Si substrates were cleaned successively with ethanol, isopropyl alcohol, acetone, and DI water. A standard AFM probe (Si_3N_4) was prepared by depositing a gold layer using a sputtering machine. The thickness of the Au layer was approximately 300nm. In Figure 3.5, the cantilever and the tip of the Au-coated probe are shown. The curvature of the tip is about 10nm ($\pm 5\text{nm}$). The length and thickness of the cantilever is 100 and 1 μm respectively.

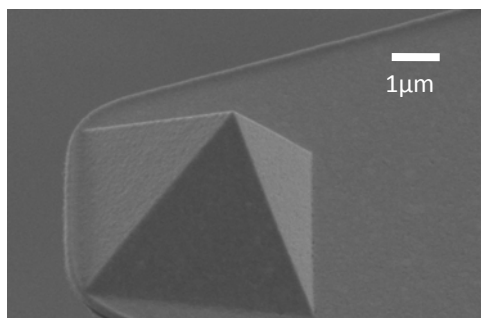


Figure 3.5. SEM image of a gold-coated AFM probe. Gold was deposited on the probe by a sputtering machine with 300nm thickness.

3.2.3. Parameters

There are four major parameters affecting the nanostructures formed by the SPAN process;

- A. The number of strokes (N)
- B. Sliding speed (v)
- C. Surface temperature (T)
- D. Down-force (F).

A. The number of strokes (N)

We have conducted a series of trails to fabricate nanostructures. It was found that the number of strokes affecting the final results. The contact-sliding of the probe induced the Au material transferred to the substrate to form a nanostructure on the Si surface. Figure 3.6 shows the structures after sliding. As increasing the number of the strokes, the height of the nanostructures increases.

As seen in the figure, the number of strokes (N) was the only variable while other conditions fixed. Here the sliding speed (v) was $480\mu\text{m/s}$ and the temperature was at the room temperature ($T=25^\circ\text{C}$). The number of strokes was determined at $N=20, 40, 50, 80,$ and 100 , as shown as the plots in Figure 3.7. In the figure, the red line is for the square-shaped structures and the blue line for the line-shaped ones. As increasing the number of strokes, the height of the nanostructures was also increased. The fact that the height of a line is higher than that of the square is due to the amount of material transfer. For line-shape, the material is built up faster than otherwise. The difference started to

show after 40 strokes. It indicates that the buildup took place only after sometime due to activation of surface molecules.

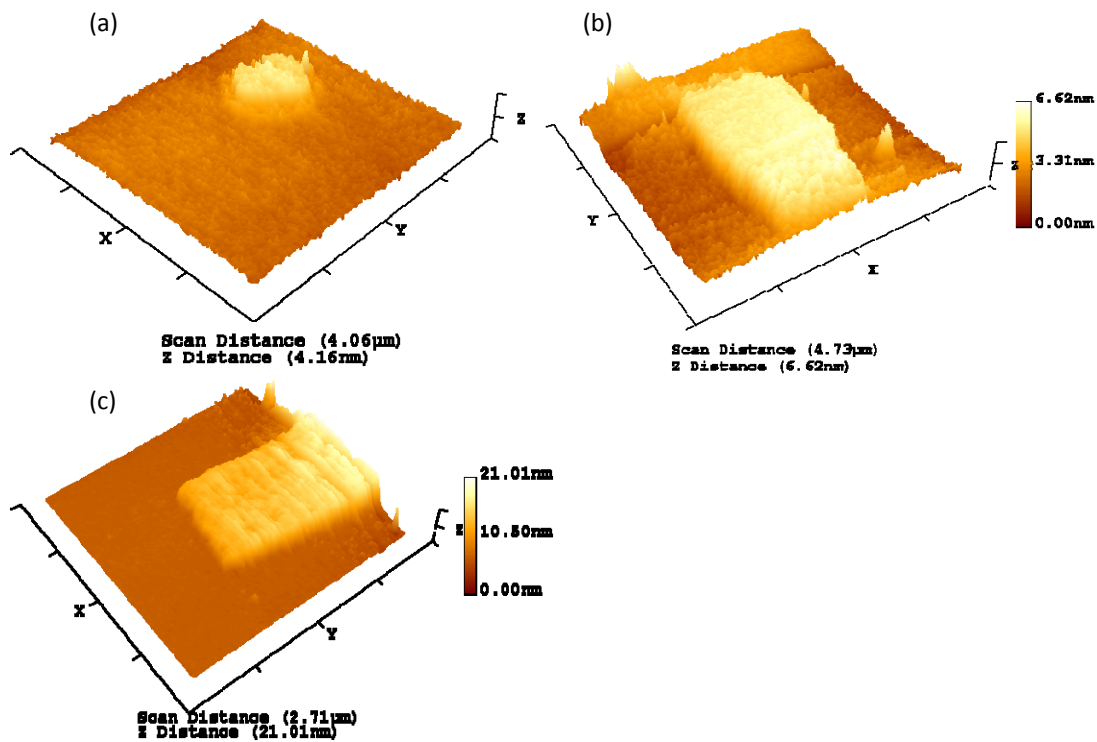


Figure 3.6. The AFM images are for nanostructures fabricated by SPAN process with different strokes (N). (a) N=50 (b) N=80, and (c)N=100.

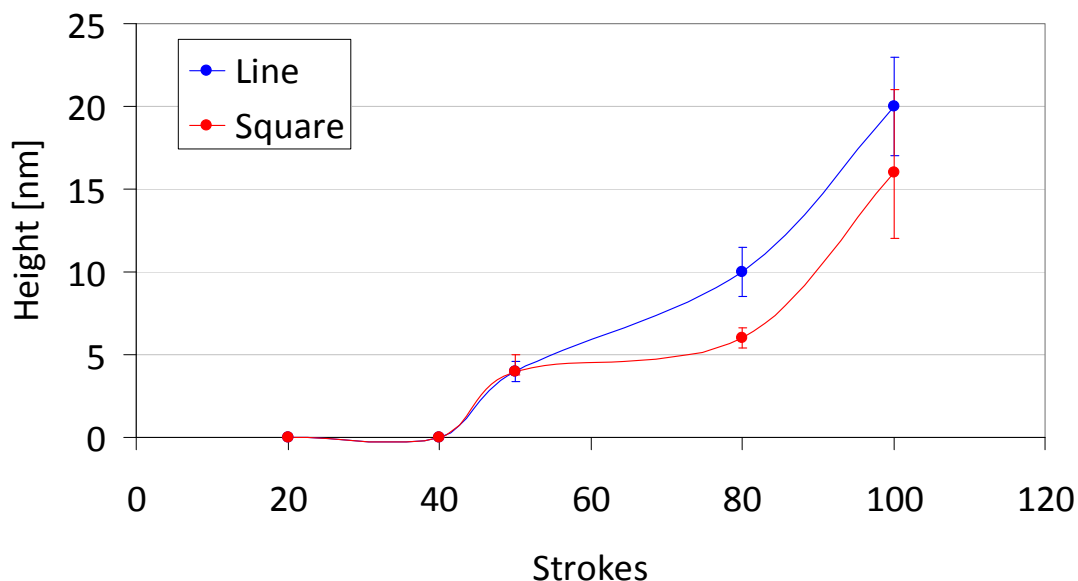


Figure 3.7. The effect of the number of strokes on nanostructure height. The red line is for the square-shaped structures, and the blue line for the line-shaped ones. Those results were obtained with fixing the other conditions at $v=480\mu\text{m/s}$ and $T=25^\circ\text{C}$.

B. Sliding speed (v)

AFM can control precisely the sliding speed and distance of its probe. Equ.2 shows the relationship between friction and normal force.

$$f_f = \mu F \quad \text{-----} \quad \text{(Equ.2)}$$

where f_f and F are friction and normal force respectively. μ is the friction coefficient. According to Equ.2, the friction is not dependent on sliding speed. However, our results showed that the contact-sliding speed affects on fabricating the nanostructures. Figure 3.8. shows the effect of the sliding speed on nanostructures. The contact-sliding speed (v) was 250, 340, and $480\mu\text{m/s}$. The sliding strokes and temperature were fixed at $N=40$ and $T=50^\circ\text{C}$, respectively. The higher the speed, the thicker the nanostructures. The sliding

speed is not directly affecting the friction. Instead, it enhances the material (Au) transfer to the Si substrate.

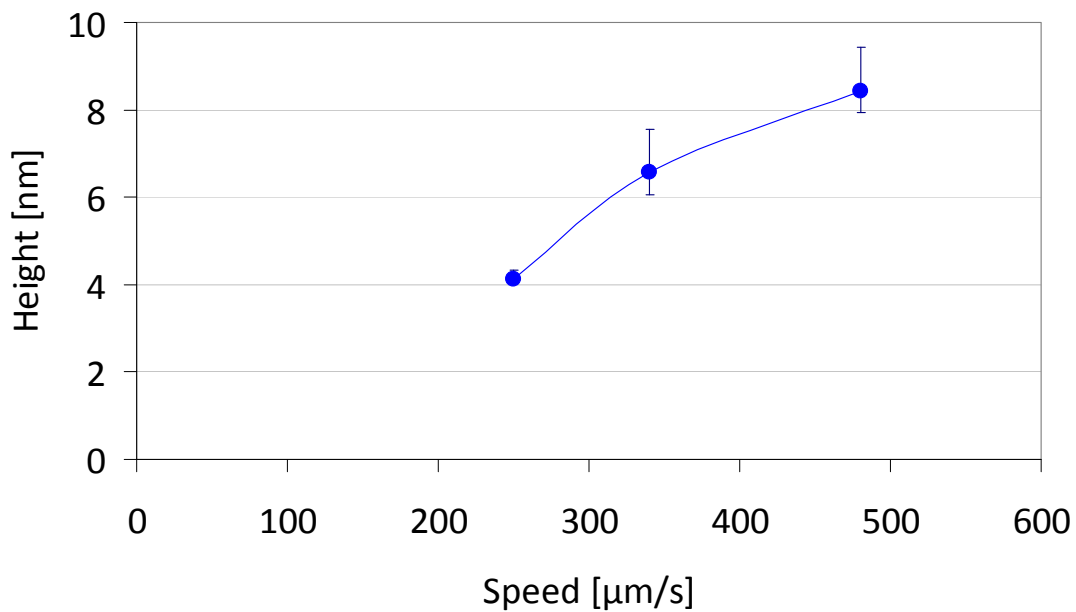


Figure 3.8. The effect of the contact-sliding speed (v) on forming the nanostructures. The sliding strokes and temperature were fixed at $N=40$ and $T = 50^\circ\text{C}$, respectively.

C. Surface temperature (T)

The sample surface temperature is controlled using a heating element. To increase the efficiency of the process, the sample stage is heated up to 100°C . As shown in Figure 3.9, the temperature of the sample surface affects the height of nanostructures. It's expected that the thermal energy enhances the interfacial interactions between Au and Si through eutectic reactions. Details will be discussed later.

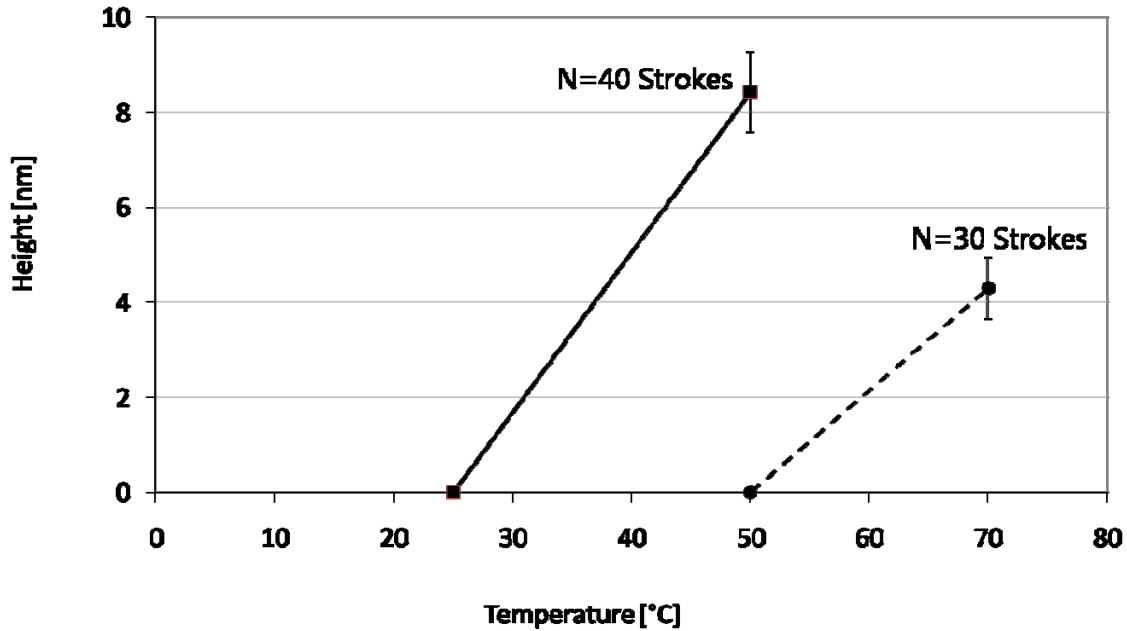


Figure 3.9. Temperature effects on SPAN fabrication of the nanostructures. The speed (v) was fixed at $480\mu\text{m/s}$, but the number of strokes (N) and temperature (T) were changed. For the solid-line, temperatures were 25 and 50°C at $N=40$. The dashed-line on the plot was obtained with the conditions; $N=30$ and $T=50$ and 70°C .

D. Down-force (F)

As mentioned in the above (Equ.2), the normal force of the probe against the substrate is affecting on friction critically. The down-forces are calculated and discussed with the cantilever deflection and the probe properties. According to Equ.2, the down-force of the probe is affecting on friction directly.

Two different forces are investigated. The line and dot structures in Figure 3.10 were obtained with $F=503.1$ and 166.7nN , respectively at $v=480\mu\text{m/s}$, $N=100$, and $T=25^\circ\text{C}$.

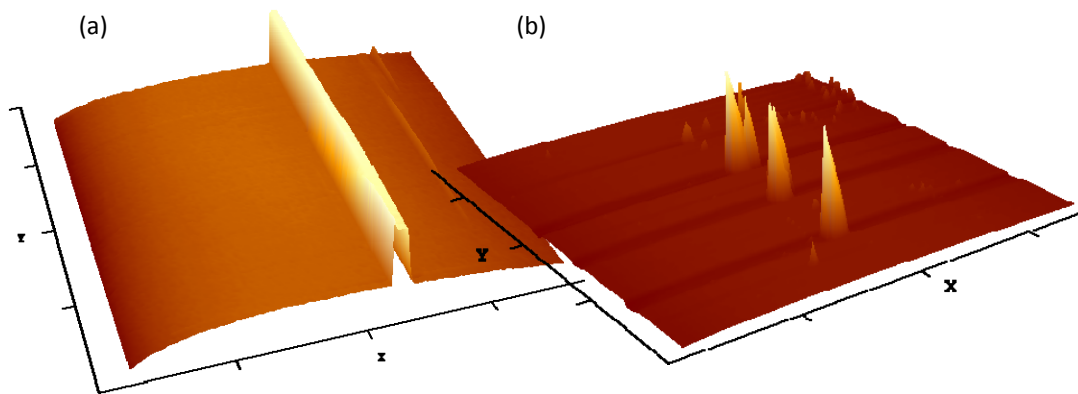


Figure 3.10. Two different down-forces (F) are applied; (a) 503.1 and (b) 166.7 nN.

The Au-coated probe was first brought into physical contact with a single-crystalline Si (100) surface. At this stage, there is no stress on the cantilever and the probe stage is considered to be at a neutral height. The probe stage is then brought down $15.48\mu\text{m}$ closer to the substrate causing a deflection of the probe cantilever.

The deflection δ of the cantilever is expressed as

$$\delta = Fl^3 / C_1 EI \quad \text{-----} \quad \text{(Equ.3)}$$

where F the bending force, l the length of cantilever, C_1 the geometry constant ($=2$), E the Young's modulus and I the moment of the cantilever.

The bending force can be solved from (Equ.4),

$$F = \delta C_1 EI / l^3 \quad \text{-----} \quad \text{(Equ.4)}$$

The length of the cantilever is $320\mu\text{m}$. Young's modulus of the Si_3N_4 probe is assumed to be 224.6 GPa. The moment I is calculated by equation (Equ.5)

$$I = bh^3 / 12 \quad \text{-----} \quad \text{(Equ.5)}$$

where the width of cantilever b is $87.5\mu\text{m}$, the thickness of the cantilever h is $0.69\mu\text{m}$ resulting in a moment of of $2.37 \times 10^{-24} \text{m}^4$. Hence the down force F applied for the nanobelts is 503.1nN and 167.7nN for dots.

SPAN is a new method to fabricate nanostructures using AFM which has advantages to overcome the problems of STM. Although a STM has advantages of high resolution and precise control, it still has disadvantages. It needs to operate in a vacuum system, and the substrates for this process have to be conductive materials. Another critical limitation is the efficiency of the processing. The processing speed is lower than other bottom-up methods. The nanowires fabricated by STM are neither uniform nor repeatable. To overcome those disadvantages of STM, AFM is suitable to fabricate nanowires with high-throughput and reproductivity. The SPAN process is based on a STM and can write a nanostructure on a substrate using AFM. Fabricating a nanostructure on a desired area is easy due to an easy control system of AFM without any vacuum system. The nanowire length is not limited with SPAN. The processing speed is improved by controlling parameters (N , T , v , F). The uniformity and reproductivity of the nanostructures were also increased. This will be discussed more detail in Section 5, Mechanisms of SPAN.

4. NANOSTRUCTURE-PROPERTY RELATIONSHIPS

This section discusses the properties of nanostructures generated using the SPAN. The characterization techniques are discussed following the experimental results. The effects of nanostructures on properties of the same are discussed at the end of the section.

4.1. Characterization

A field emission scanning electron microscope (FE-SEM, LEO 1530 VP) and a scanning electron microscope (SEM, JEOL T330) were used to characterize the gold-coated probe before and after contact-sliding experiments, as shown in Figure 3.5. The total height of the tip, including the coating, was $8\mu\text{m}$, and the base of the tip was a square shape with side of $8\times 8\mu\text{m}^2$. The images were obtained at a magnification of $\times 7,500$ with the accelerating voltage 30kV. The wear scar diameter was measured from the SEM images.

The properties of the fabricated structures include the physical size, roughness, hardness, electrical conductivity, and adhesion. The hardness was characterized by using a nanoindenter (Hysitron TriboIndenter). The nanoindenter could provide the submicron scale measurement for the hardness with real-time data. Mechanical properties such as roughness, adhesion, and size of the microstructures were measured using the AFM. Adhesion forces were characterized by using the AFM standard (Si_3N_4) tips for both tip and substrate materials. Roughness and physical dimension of the microstructures were estimated using the close-contact mode AFM tips. The properties of the processed probes and substrates were characterized by using the nanoindentation, the energy dispersive X-

ray spectroscopy (EDS), the transmission electron microscopy (TEM), and the scanning electron microscopy (SEM) techniques. The same AFM, with a different problem was used to characterize the topography and phases of the substrate surface. To prevent any damage on the sample surface, a non-contact mode probe was used for the analysis. Electrical properties of nanostructures were obtained by using an AFM conductive probe in the same setup. The configuration is shown in Figure 4.1. The conductive probe was operated in a contact mode to drive electrical current flow through. A picoameter was employed to record the electrical current in real time.

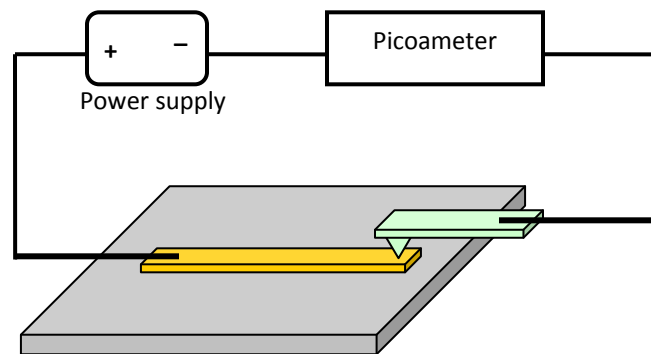


Figure 4.1. Experimental setup to characterize the electrical properties of the patterns.

4.1.1. Atomic Force Microscope (AFM)

AFM can obtain the surface properties, such as topography, phase, or conductivity. The AFM probe size is fitted to characterize nano-scale properties so that the nanoscale features can be imaged. Additionally, the AFM has a function to measure the adhesion force between a sample and the AFM probe. The adhesion force is calculated from a force-distance curve of the probes. That's how to get AFM images and mechanical

properties of the nanowire. Moreover, the AFM provides a way to characterize the electrical property of the nanowires. The AFM conductive probes can be utilized to measure the conductivity of the nanowires, because the curvature radius of the probes is less than 15nm. The I-V curve of the nanowires can be plotted by AFM conductive probes.

4.1.2. Scanning Electron Microscope (SEM)

The AFM probes are expected to be changed before and after the process of SPAN. SEM will be usable to investigate the probes. Figure 4.2 shows two SEM images of AFM probes. The first SEM image is an AFM original probe which is not coated with gold. The material of the original one is Silicon Nitride (Si_3N_4). The right side SEM image is an Au coated AFM probe.

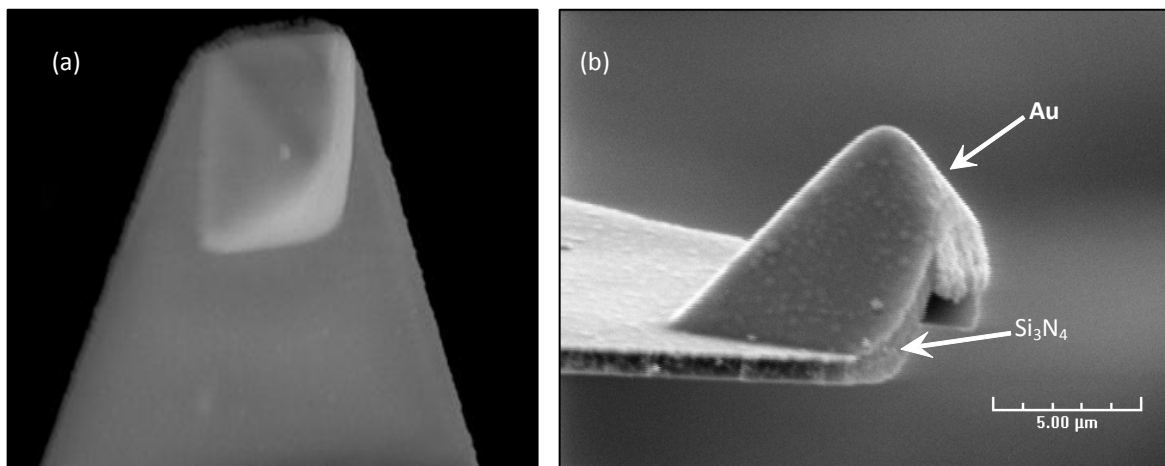


Figure 4.2 SEM images of AFM probes before and after coating gold on it, (a) and (b) respectively.

The Au-coated probes will slide on silicon substrates during the SPAN process. After sliding, the probes should be changed. It will be investigated in detail by using an SEM.

4.1.3. Nanoindentation

Mechanical properties will be obtained. A Hysitron Nanoindenter will be used to measure the hardness of the nanowires. The machine is a low load nanomechanical test system. The diamond tip of the machine is indented on the nanowires gradually. It will display the load of the diamond versus displacement of the nanowires which is called a load-displacement curve. The machine calculates hardness and elastic modulus of the nanowires from the curves.

4.1.4. Transmission Electron Microscope (TEM)

The atomic resolution of the nanowires will be achieved by TEM (JEOL JEM-2010). The imaging resolution of the machine is 0.23nm. Diffraction patterns of the nanowires will be obtained to investigate the atomic structures of the nanowires. Also, the interfacial areas between the nanowires and silicon substrates will be resolved. The machine has the Energy-dispersive X-ray spectroscope (EDS). EDS is usually used for the elemental analysis or chemical characterization. The compositions of the nanowires will be investigated by EDS.

4.1.5. TEM Sample Preparation

TEM samples were prepared for interface analysis of the substrates and nanostructures. The procedure of preparing the TEM samples is shown in Figure 4.3 (a-f). First two substrates (gray-colored) with the formed nanostructures (yellow color) were prepared as described in the previous section, shown in Figure 4.3(a). The two substrates were glued together (Figure 4.3(b)) with the nanostructures facing each other. The glue (red color) filled the voids between the two substrates. The sample was ground until it reached a length of $60\mu\text{m}$, as shown in Figure 4.3(c). Dimpling was processed until the thickness reached $20\mu\text{m}$, as shown in Figure 4.3(d). An ion miller (Precision Ion Polisher System 691, Gatan) oriented as shown in Figure 4.3(e) was used to make a hole at the center of the sample. The hole was used to verify that the appropriate thickness of 20nm was obtained for TEM analysis. One of the TEM samples was shown in Figure 4.3 (f). The outer ring was a sample holder protecting the two substrates.

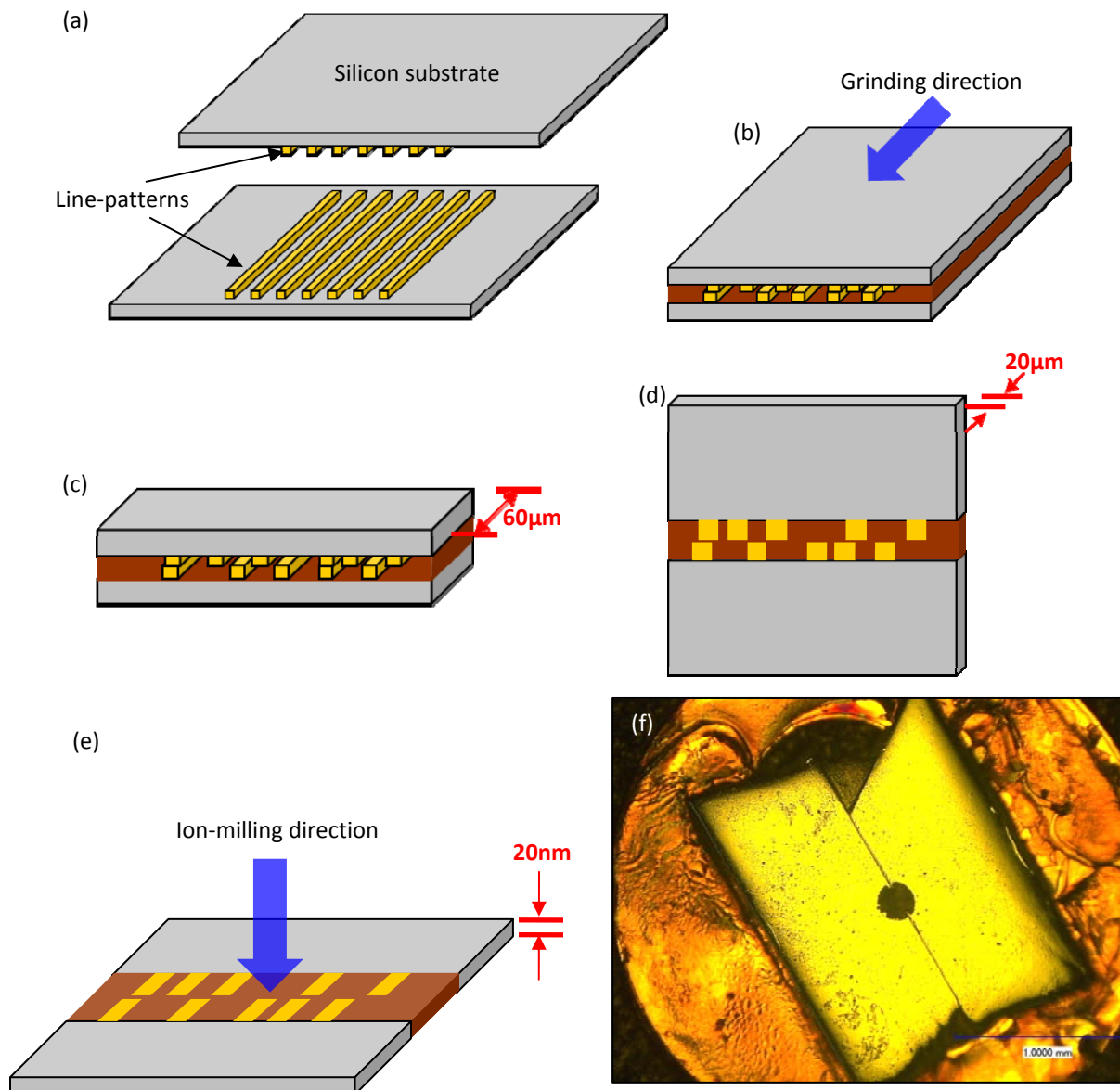


Figure 4.3. The procedure of preparing the TEM samples is shown in (a)-(f). (a) Two substrates (gray-colored) having the patterns (yellow) were prepared. The substrates were glued together. (b) The length of the sample got smaller by grinding along the direction (arrow). (c) When the sample got $60\mu\text{m}$ in length, dimpling was started. (d) Dimpling stopped at the length of $20\mu\text{m}$. (e) An ion miller was used to make a small hole at the center of the sample by emitting ions on the side of the substrate as the arrow. The final thickness of the sample around the hole was 20nm . (f) The real TEM sample was taken by a microscope. An outer ring round the sample was a sample holder protecting the two silicon substrates separated.

4.2. Nanostructures

After a number of contact-sliding strokes, the Au-coated probe was observed and it showed a visible wear scar (Figure 4.4). Under the SEM, the scar is smooth with neither fracture nor a large area of Au material pullout.

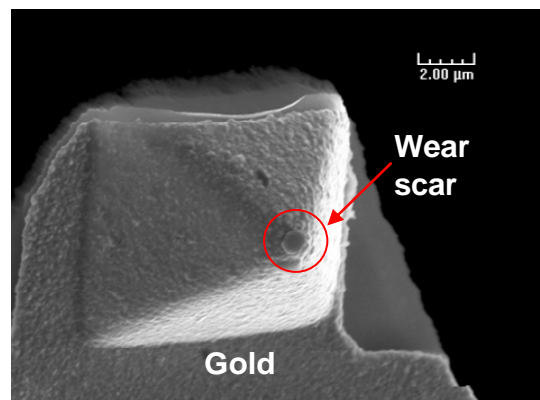


Figure 4.4. SEM image of an Au-coated AFM tip after sliding-contact experiments.

On the Si surface, there are some microstructures formed. Figure 4.5a shows a square grown due to sliding of Au probe on Si in the area of $1.5 \times 1.5 \mu\text{m}^2$. A standard Si_3Ni_4 probe was also used to slide on the Si sample in a designated area. Comparing the grown-square fabricated by Au probe, the square area on Si scanned by the standard Si_3Ni_4 was not grown as shown in Figure 4.5b. After the first few strokes for line sliding tests with the Au-coated probe, discontinued dots were found on the Si surface (Figure 4.6a). A line started to form connecting the aligned dots with different height. The peaks outside the line are thought to be debris on the original Si surface. After 80 times contact-sliding, a continuous line formation is observed on the Si surface (Figure 4.6 b-d). The morphology image (Figure 4.6b), phase image (Figure 4.6c), and the 3D image (Figure

4.6d) show a clean and straight line. Another line-structure was fabricated with the same conditions shown in Figure 4.7. The structure was also observed by AFM. The top-view, phase, and 3D images are shown in Figure 4.7 (a), (b), and (c) respectively. The top portion of the line (Figure 4.7c) is slightly narrower than that at the bottom. The width of the line-structure is about 600nm and the height is 12nm.

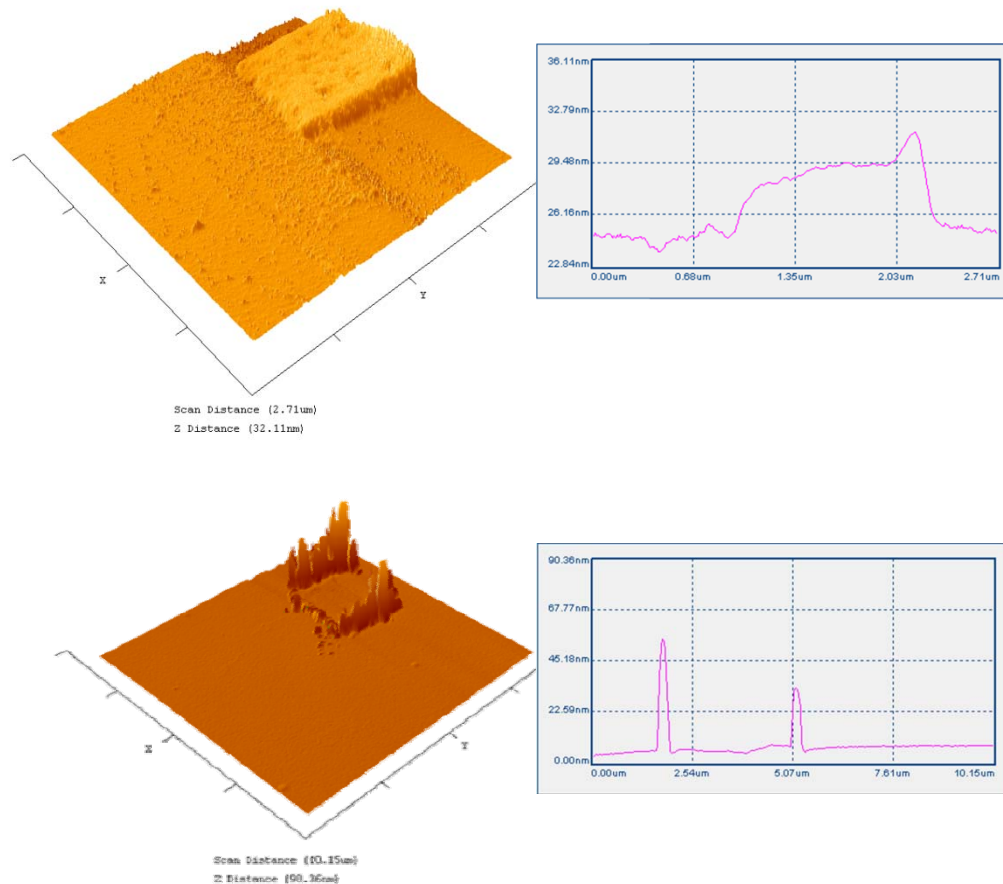


Figure 4.5. 3D AFM image and profile (left to right) (a) A square pattern was grown by sliding of Au-coated probe at room temperature. (b) The square contact-sliding area of Si was not affected by sliding of Si_3Ni_4 (normal) probe.

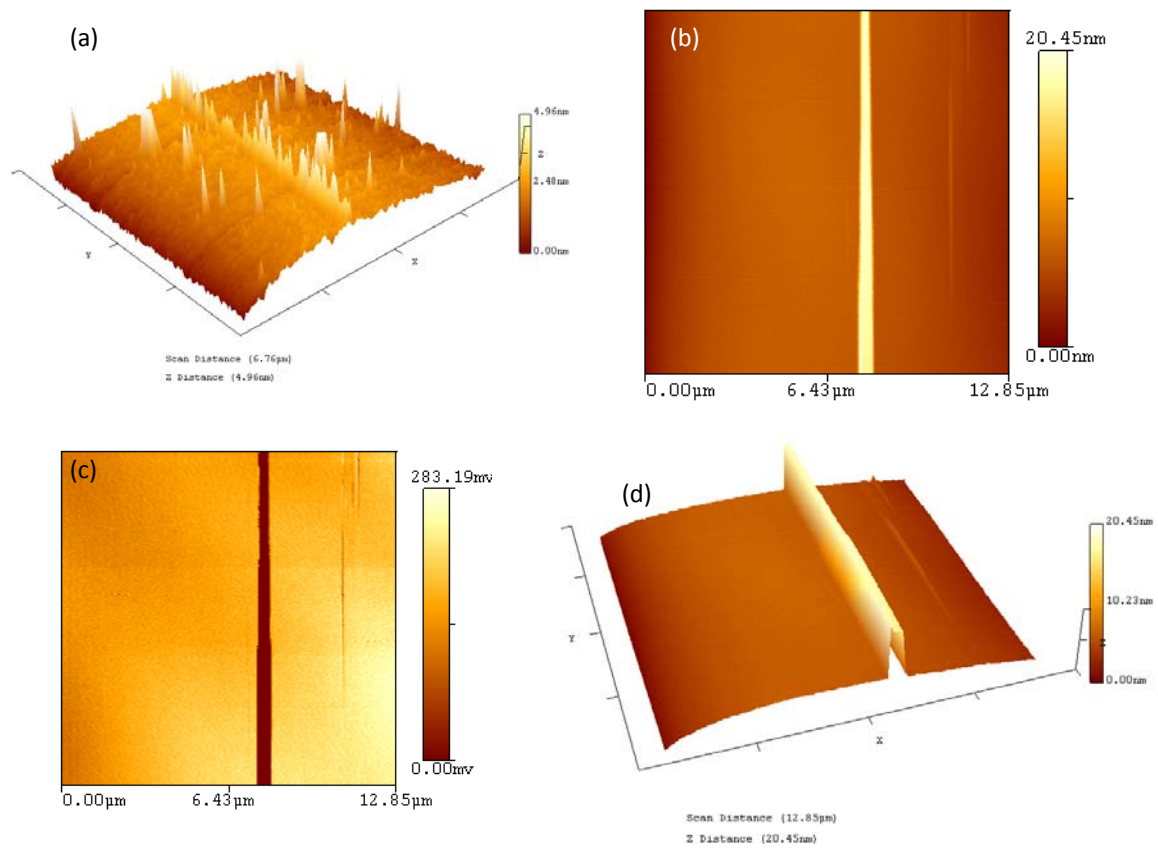


Figure 4.6. AFM images of line-structures formed on the silicon substrate. (a) Initial stage of line-forming. (b) Top view of the line structure. (c) Phase image showing the width variation. (d) A 3D view.

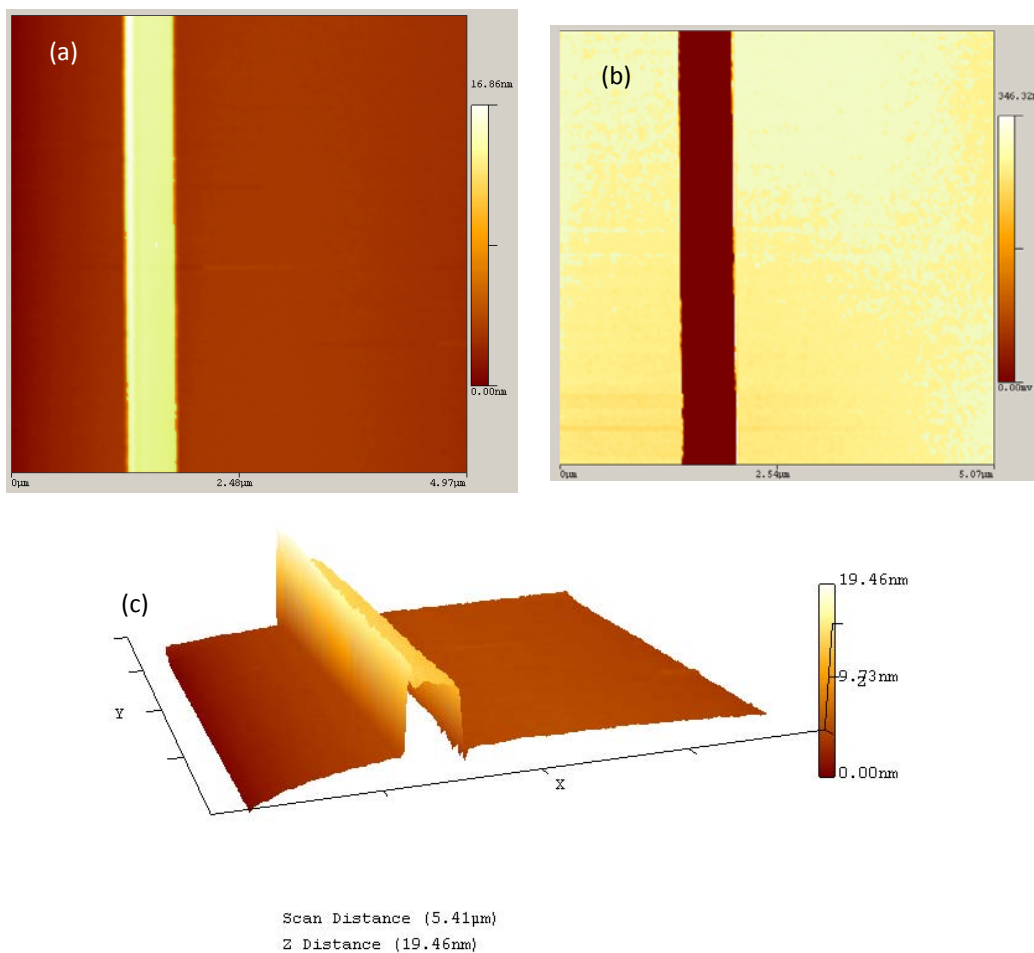


Figure 4.7. The line-structure was observed using an AFM. (a) Morphology image. (b) Phase image. (c) The 3D image.

A non-contact mode AFM probe was used to characterize the morphology of the nanostructures created. Figure 4.8 shows a SEM image and a line-shaped structure formed by the SPAN process. The SEM image in Figure 4.8 (a) is the AFM gold-coated probe after the sliding test. The gold on the probe was transferred to the substrate to form the structures. The gold-coated probe revealed wear as demonstrated by the blunt tip shown in Figure 4.8(a). The transferred gold can be identified on the nanostructures by EDS. It will be discussed in the next section. The AFM images in Figure 4.8 (b, c, and d) show the height, profile, and 3-dimensional images of a nanobelt formed by the process. The nanostructure was created by sliding the probe with an initial 503nN down-force and 480 μ m/s for 80 strokes. The structure has 10nm height and 600nm width which coincides with the width of the worn surface of the probe in Figure 4.8(a).

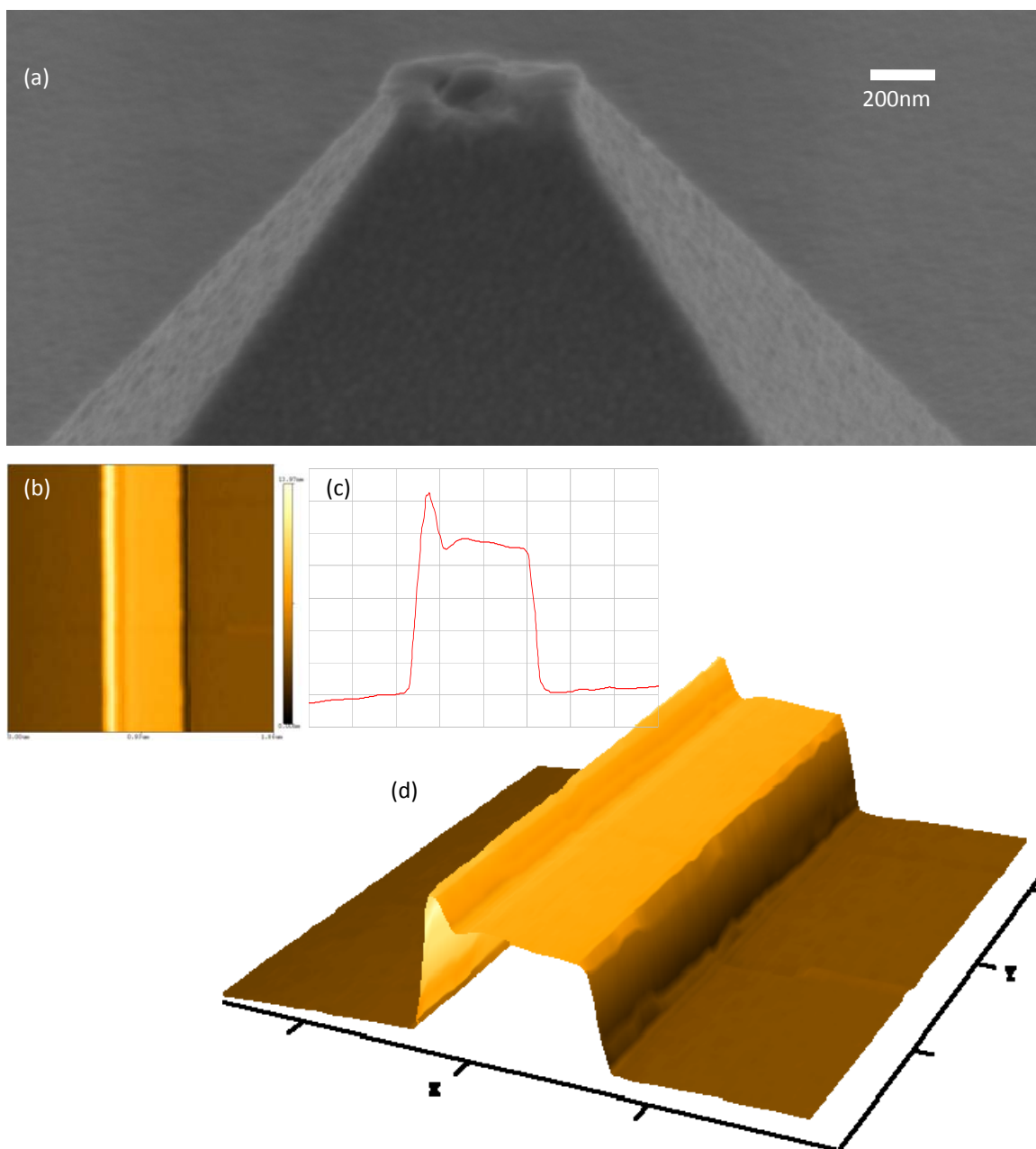


Figure 4.8. SEM and AFM images after test. (a) SEM image of the gold-coated probe after a contact-sliding test. AFM images of the nano-sized line on the Si substrate; (b) topology, (c) profile, and (d) 3D AFM image. The dimension of the line is 10nm high and 600nm wide.

The down force of the probe could not be measured during the test due to the limitation of the equipment. The down force was calculated by the tip deflection and will be discussed later. Another test was conducted with the same conditions as the previous test, except the down-force of the probe was reduced to one-third. The sliding with these conditions formed dots instead of a nanobelt.

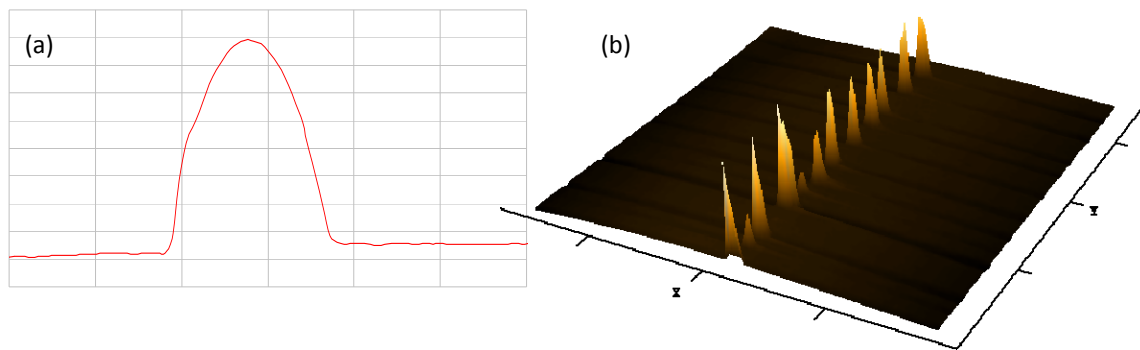


Figure 4.9. A series of dots fabricated by an intermittent-contact test. (a) a profile shows the height and width of one of the dots; 153nm high and 400nm wide. (b) a 3D image of the dots along the sliding direction of the probe.

Figure 4.9 (a) shows the profile of a dot. The height and width of the dot were 153nm and 400nm, respectively. The dots were laid along the sliding direction of the probe as shown in Figure 4.9 (b).

4.3. Effects of Applied Potential on Nanostructures

The SPAN technique was verified to fabricate various shaped patterns. The sliding tests were conducted to fabricate the Texas A&M University logo “ A_T_M ” on the substrate,

shown in Figure 4.10. For the tests, electrical potential of 12V was applied between the substrate and the probe. Figure 4.10 is the results of the tests. The pattern, ${}_{\Lambda}T_M$, is shown in Figure 4.10 (a) and the ‘A’ letter of the pattern was enlarged in Figure 4.10 (b).

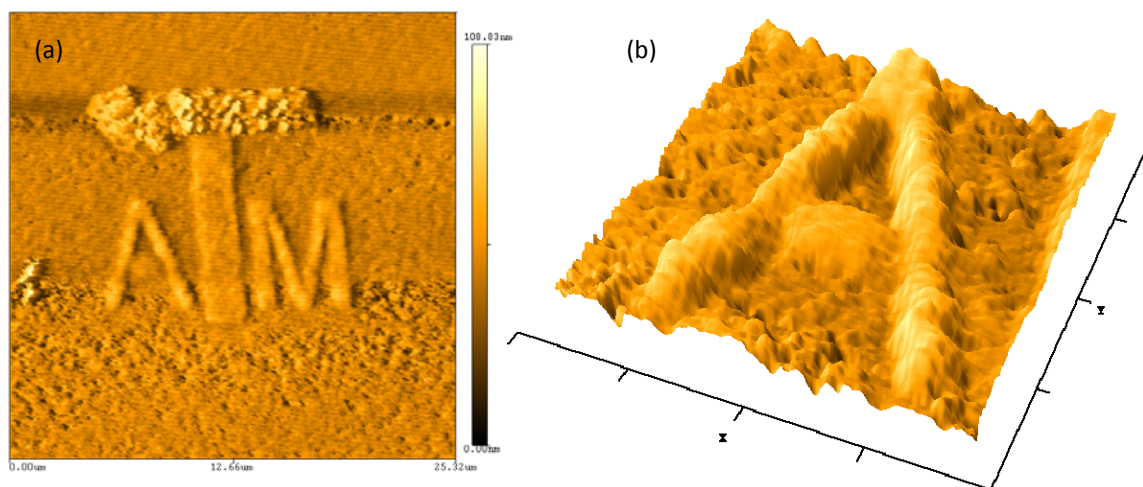


Figure 4.10. ${}_{\Lambda}T_M$ formed by the contact-sliding test with electrical potential (12V). (a) AFM height image of the pattern. (b) Enlarged image for ‘A’ letter of ${}_{\Lambda}T_M$ pattern.

The ${}_{\Lambda}T_M$ pattern in Figure 4.10 was fabricated with the same conditions as the nanobelt but with one addition. An electrical potential of 12V were applied between the probe and the substrate during the sliding tests. In both Figure 4.8(d) and 4.9(b), the width of the structures is approximately 600nm. The smallest width of the pattern (Figure 4.10) formed with the electrical potential is 800nm. The applied electrical potential leaded the structure wider. Electrical adhesion, defined by McLean [147], was originated at the contact point due to highly concentrated electrons. Here, the applied electric field is highly localized at the contact area of the probe on the substrate. The large electrical

adhesion was induced due to the highly intensified electrical strength between the two materials. According to the theoretical equation,

$$\sigma = (\varepsilon_0 \varepsilon_a / 8\pi)^{1/3} [VEd / \pi D^2 (1 - \nu^2)]^{2/3} \text{ ----- (Equ.6)}$$

the electrical adhesion stress σ could be increased with the electrical potentials [148] where ε_0 is the permittivity of free space, ε_a the average permittivity, V the applied electrical potential, E the young's modulus of the probe material, d the contact diameter, D the contact length on substrate, and ν the Poisson ratio of the probe. The adhesion force was increased on the highly localized contact area by electrical adhesion. This further enhanced the gold material transfer resulting wider patterns.

4.4. Volume

Our results showed that the Au material on AFM tips was transferred by sliding. The amount of transferred material was compared with the loss of the coated material (Au) on AFM tip. The sliding experiments were conducted in extended sliding lengths (80, 120, and 160 μm) for accurate estimation. As shown in Figure 4.11, the volume of the wear scar of Au-coated tip (gold loss volume) was 5.65, 9.48, and 12.01 μm^3 at the contact-sliding distance of 80, 120, and 160 μm respectively. However, those of grown lines on Si substrate were 6.85, 10.28, and 13.7 μm^3 at their respective distance. These results showed that line-structure volumes on the Si surface are larger than the gold loss of the tip. This might mean the existence of a different type of material in the generated line-structures. Further evidence was sought in continued characterization.

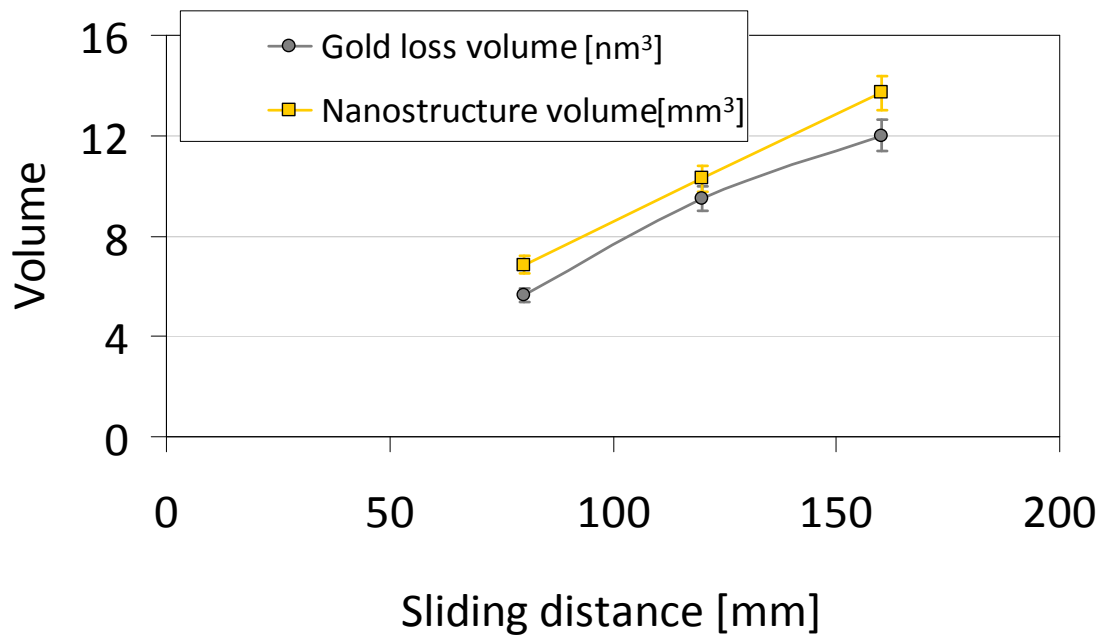


Figure 4.11. The extended experimental data comparing the wear scar (gold volume) on the Au-coated AFM tip with line-structure volume versus the sliding distance.

4.5. Surface Properties

Hardness, Roughness, and Adhesion are discussed here. To compare the line-structures with Si substrate, mechanical properties were characterized. Roughness was characterized by the AFM which was used for sliding tests. Roughness of Si and the line-structure were 2.22 and 0.7nm. The surface was smoothed by sliding the AFM tip on it. Hardness was measured by a nanoindenter. Hardness of Si was 150GPa while the line-structures were 78GPa. Although the roughness of the line-structures was smoother than that of Si, the structures have the lower hardness value than Si. Adhesion force was calculated with the force-distance curve for the AFM tip and the sample surface. The line-structures and Si have their adhesion value, 217.74 and 213.68nN respectively.

4.6. Electrical Conductivity

To understand the nature of microstructures, we conducted electrical conductivity measurement using the AFM. Electrical potentials were applied and the current flow through a conductive AFM tip to the small lines. One end of the line was applied to the negative electrical potential while the probe was with a positive (+12V). Figure 4.12(a) shows the experiment setup to measure the conductivity. The conductive AFM tip was scanned by reciprocating between the silicon and the line regions on the surface as shown Figure 4.12(a). The current was generated during scanning. The amplitude of the generated current was different with the conductivity of the surface. The flown current throughout the line and the AMF tip was displayed by a picoameter, recorded and plotted by LabView program on a computer. Figure 4.12(b) is its result. The y-axis is the amplitude of the current and x-axis is the scanning time up to 1 second. The peaks were occurred when the probe was on the line-microstructure during scanning. There was no peak for the probe on the Si substrate. The average currents flowing on gold line and silicon surface are $11.22\mu\text{A}$ and 0.41nA respectively.

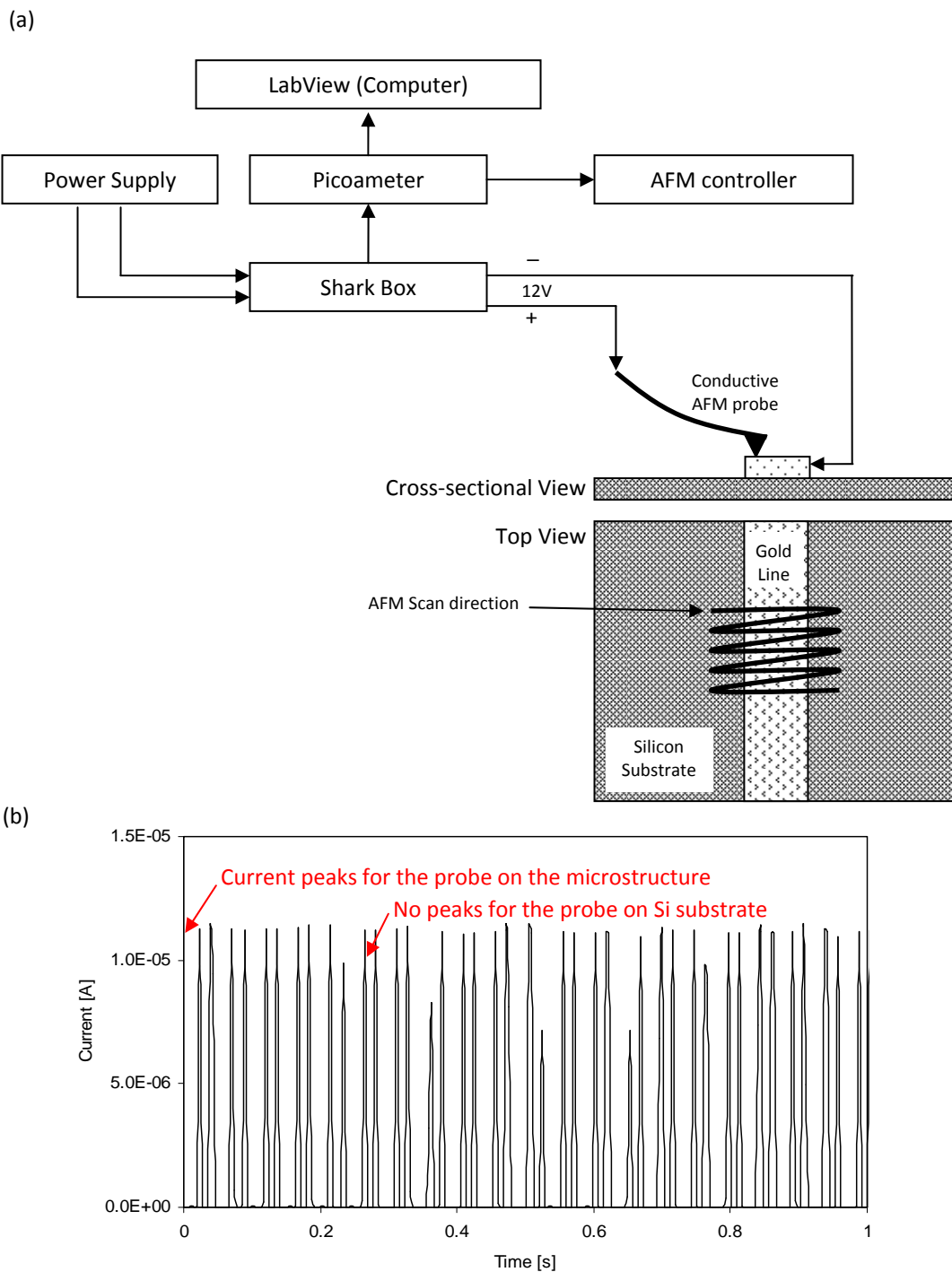


Figure 4.12. Electrical conductivity measurement. (a) The simple diagram of the experimental setup to measure the conductivity. (b) Current flowing throughout the line-structure. During scanning, the current peaks were occurred when the AFM conductive probe was on the microstructure; no peak for the probe on Si substrate.

Electrical properties of the nanostructure in Figure 4.8 were characterized using an AFM conductive probe. As shown in Figure 4.1, an electrical potential was applied between the nanopattern and the probe to observe the conductivity of the structure. The output current was measured and recorded by a picoameter.

Figure 4.13 shows the measured current as the probe scanned across the width of the structure. The x-axis is for the position [μm] of the scanning probe across the sample and the y-axis for the output current [A]. The insert in Figure 4.13 is the height profile of the nanostructure used for electrical property measurements. The average current value of the nanostructure is $1.07 \times 10^{-3} \text{ A}$. On the other hand, when the probe was in contact with Si, the current was about 0A. I-V curves of the nanopattern, Si substrate, and pure gold were also obtained using the same setup, varying the potential from -2 to 2V with 0.01V increments. The current flow of the Si substrate (blue line in Figure 4.14) was about 0A. The conductivity of a pure gold layer deposited by sputtering methods on a Si substrate (green line) shows a linear and high response to changes in voltage. However, the output current for the nanostructure (red line) exhibited a nonlinear response due to a semi-conductive property caused by the alloying during the SPAN process.

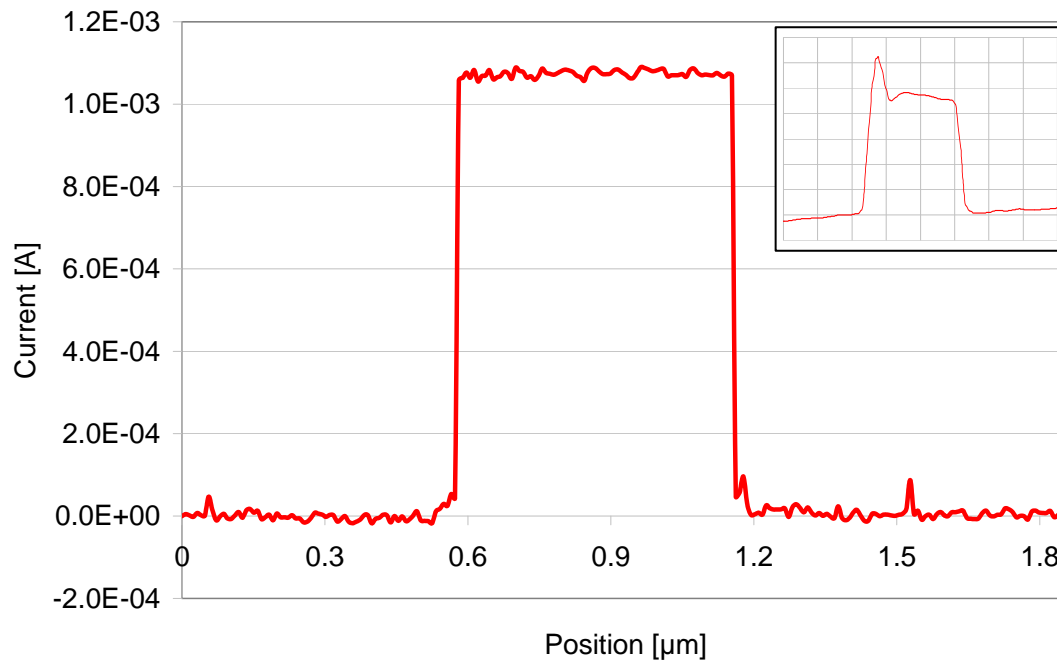


Figure 4.13. The profile of output current [mA]. When an electrical potential (12V) was applied to the nanostructure, the electrical current flowing through was recorded. The average current is 1.07×10^{-3} A. The inset of the figure is for the profile of the nanostructure (cross-sectional view of the structure).

As shown in Figure 4.14, the conductivity of the nanostructures formed by the SPAN process was compared with the substrate and a pure gold layer. The pure gold layer has the resistance of 2.88Ω . The current of the pure gold layer increased proportionally with the input voltage. On the other hand, the output current (red-line) for the nanostructure was increased nonlinearly while the input voltage was increased linearly.

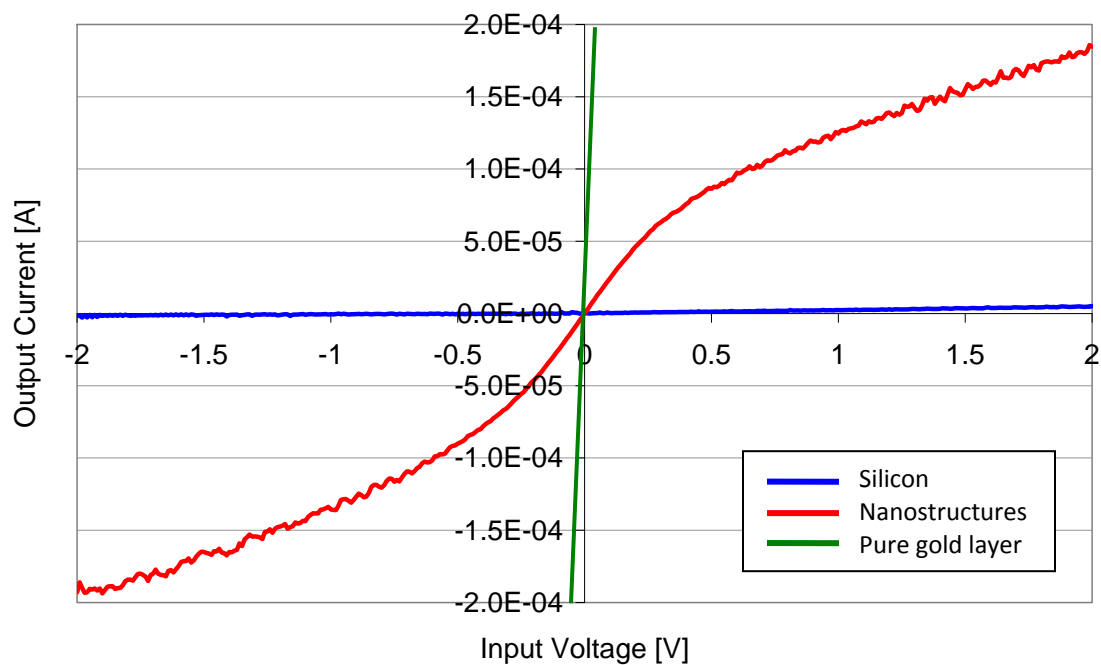


Figure 4.14. I-V curves of the nanopattern and a Si substrate. For each material, the applied electrical potential was increased from -2 to 2V with 0.01V step. The current (blue-line) flowing throughout Si substrate was about 0A, but the slope of I-V curve of the pure gold layer is very high. Meanwhile, the output current (red-line) for the nanostructure was increased nonlinearly even though the input voltage was linearly increased. The resistances of the nanostructure are $R_{low}=5k\Omega$ and $R_{high}=14.3k\Omega$, respectively.

5. MECHANISMS OF SPAN

5.1. An Alternative Nanolithography Process

In the present study, a new method to fabricate nanostructures, so called SPAN, has been developed. The SPAN method is similar to writing on a paper with a pencil. The coated-AFM probe acted as a pencil. This process is different from any other AFM based methods in that the nanostructure is created from an alloying effect of two materials. The SPAN can be seen as a combination of the AFM Oxidation lithography and the dip-pen lithography without the metallic ink. AFM Oxidation lithography applies electrical potentials without a mechanical force to form a nanostructure.

A probe of 10nm (± 5 nm) diameter is used for the SPAN method. The Hertzian contact pressure is about 6.4nN/nm^2 , consistent with previously reported data [149, 150]. The surface tension (γ) of silicon is 700mN/m , and the force F is followed the previously reported equation; $F = 4\pi r\gamma \cos(\theta)$ where r is the probe diameter and θ is the contact angle of probe to the surface which is approximately 0. The sliding leads to friction and shear of the tip [151, 152]. The frictional energy introduced through sliding is transferred to the thermal energy increasing the temperature at the tip, referred as the flash temperature [153, 154]. At a certain temperature, the tip material is transferred to the substrate and the two materials form an alloy together at the nanoscale [155, 156]. The down-force of the probe leading to friction and shear is an important factor for the process.

There are other factors affecting the dimension of the nanostructures: the shape of the probe and the surface roughness of the substrate. With observation of worn AFM probes (results not shown), the tip of the AFM probe was a sharp pyramidal shape as shown in Figure 5.1.

Figure 5.1 is the simple diagrams for the probe before gold coating, after coating before the sliding test, and after the sliding test. The curvature of the tip was 100nm after gold coating. After sliding, the wear of the tip caused the end to become blunt. The nanostructures had the corresponding shape of the probe as shown in Figure 5.1(c). The Au coating on probe was worn out due to contact-sliding. However, it did not expose the original AFM probe (Si_3N_4) as demonstrated by EDS analysis of the probe (Figure 5.1(d)) and nanostructure (Figure 5.2). Nitride (N), one of the materials of the original AFM probe, was not found on either probe or nanostructures. Figure 5.2 shows that the nanostructure is not purely gold, rather it was composed of 72.7%wt. Si and 27.3%wt. Au.

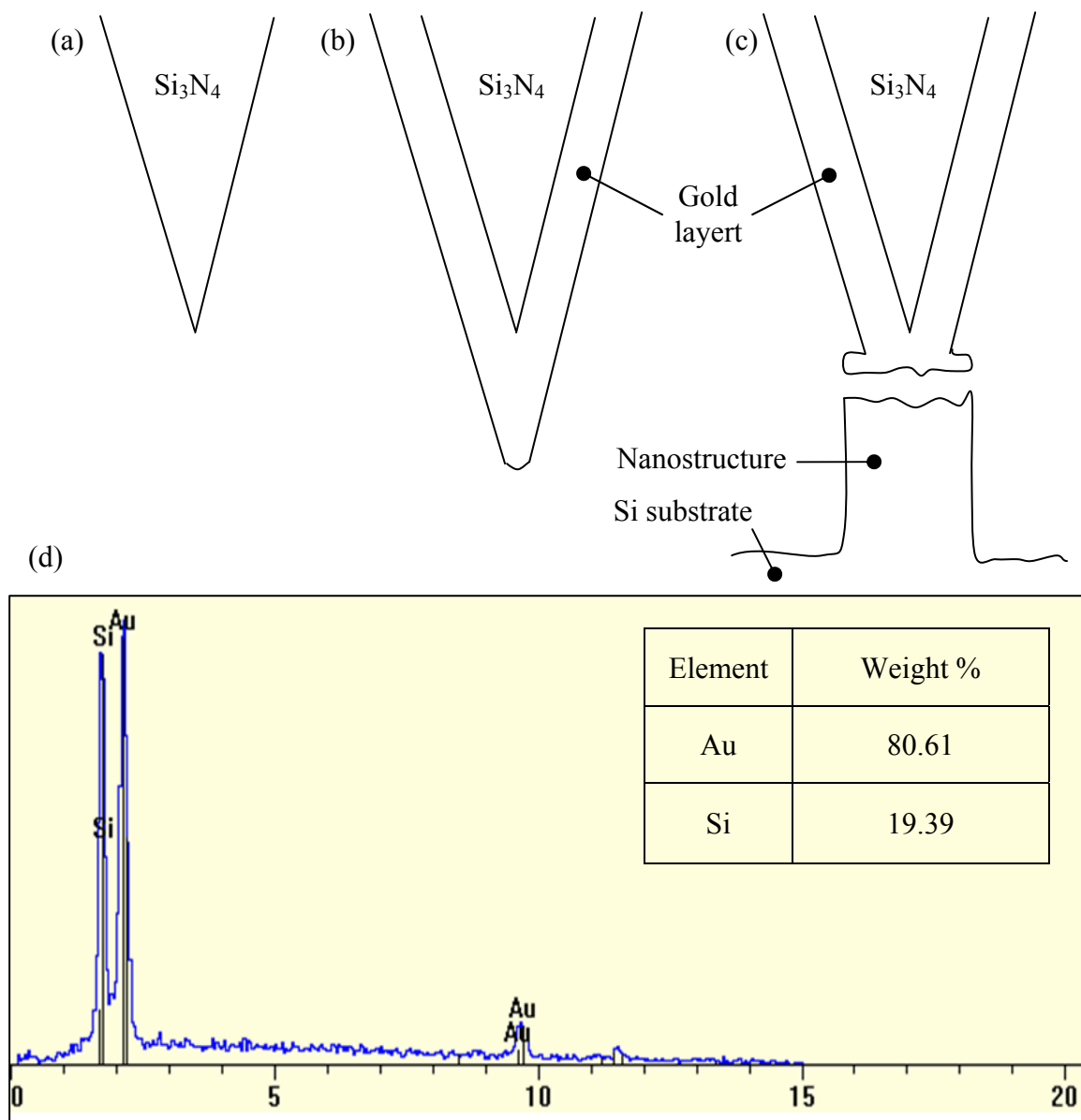


Figure 5.1. Schematic diagrams for the AFM probe (a) before gold coating, (b) before the sliding test with coating, and (c) after the test. The nanopatterns on the substrate were built by sliding the coated probe. As shown in (c), the nanostructures had the corresponding shape of the probe. (d) is the energy dispersive x-ray spectroscopy (EDS) analysis for the probe after sliding test.

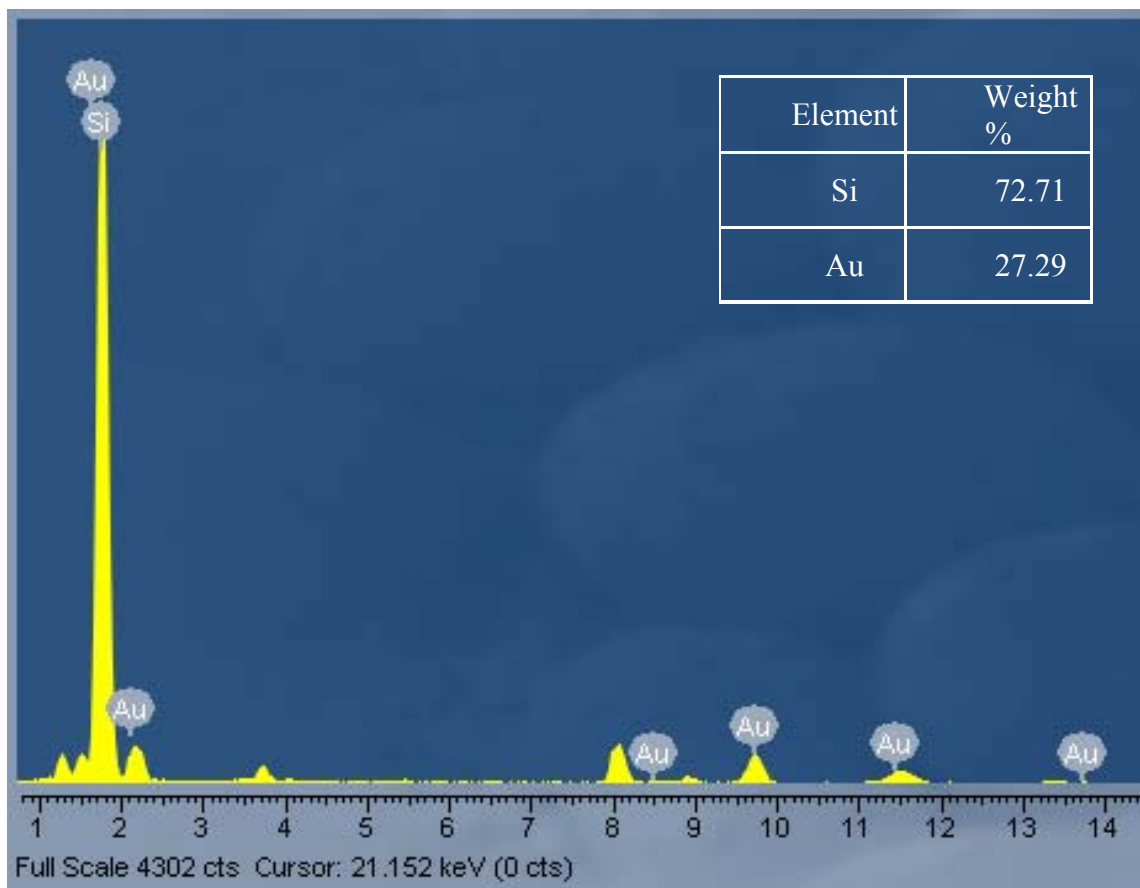


Figure 5.2. EDS data showing the composition of the nanostructure. It has 72.71%wt. of Si and 27.29%wt. of Au.

A TEM image of the cross-section of the nanostructure is shown in Figure 5.3. The boundary between the nanostructure and the Si substrate is not distinctly clear. The highly localized down-force and frictional energy activates gold atoms to diffuse into the Si substrate forming an alloy. The mixed layer of Au-Si under the substrate is only a few atomic layers thick. With further sliding of the probe on the substrate, the nanostructure was grown on the substrate. The left-inset is one of the cross-sectional images, and the right is the diffraction pattern of the structure.

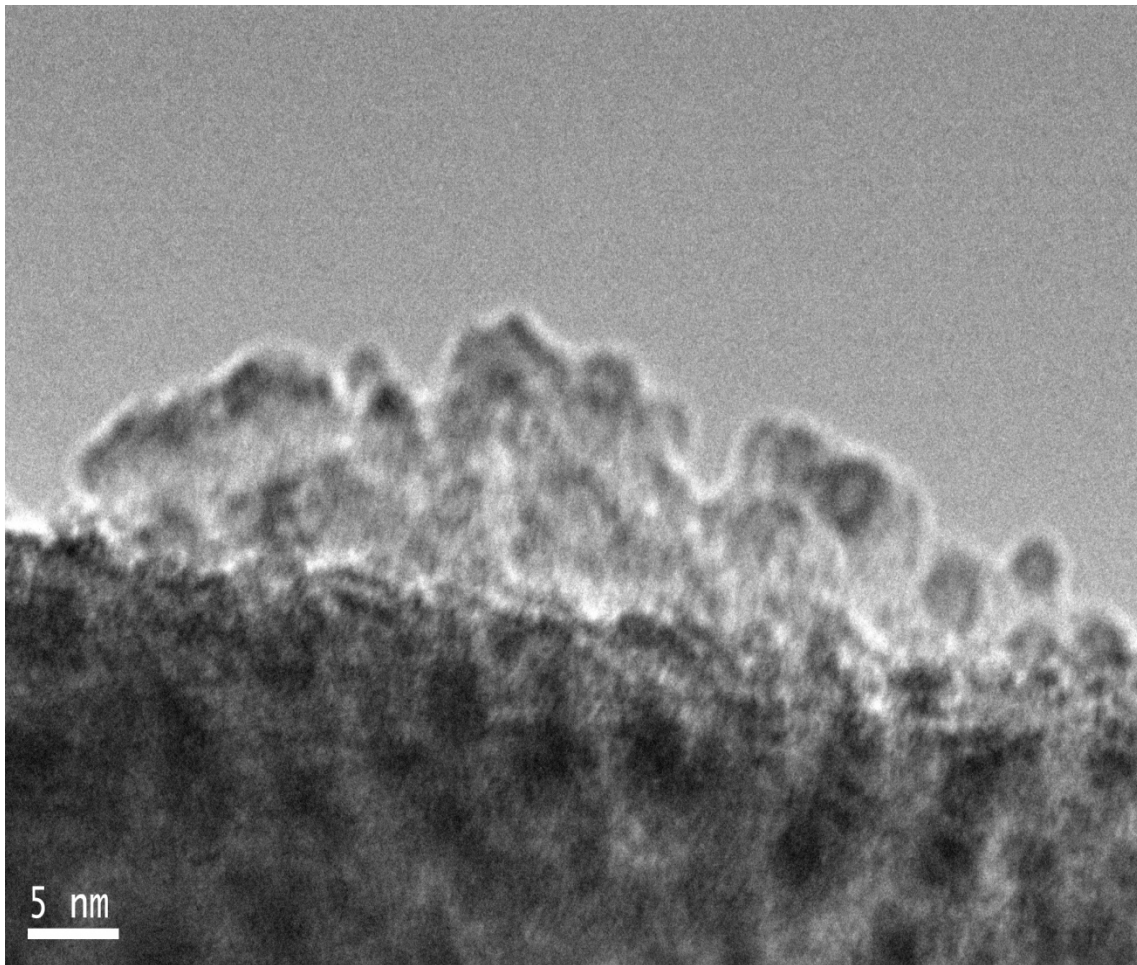


Figure 5.3. TEM images of the interface of the nanostructure and the silicon substrate.

5.2. Electrical Conductivity of Small Structures

The nanostructure in Figure 4.8 was characterized for its conductivity. Results are shown in Figures 4.13 and 4.14. As mentioned earlier, the electrical current through the nanostructure did not respond linearly with the input voltage. That means that at low electrical potentials (from -0.3 to 0.3V), the nanostructure has a low resistance (R_{low} , high current), and a high resistance (R_{high}) at high potentials. The resistance is 5 and 14.3k Ω for low and high potentials, respectively.

Much like strain gages and thermocouples vary resistance in response to a mechanical deformation and thermal variations [34, 35, 36], the nanopatterns on Si respond to an applied electrical potential. The resistance (R) of the material depends on the length (L), cross-sectional area (A), and the resistivity (ρ) as shown in the following equation.

$$R = \rho L / A \text{ ----- (Equ.7)}$$

Here the length (L) and the cross-sectional area (A) of the nanostructures were maintained to be constant. The resistivity (ρ) could thus dominate the resistance. According to Ohm's Law, it has been known that the current should increase with the voltage. The charge passing through the nanostructure is hence increased with the increased electrical potential. It means that more electrons pass through the nanostructure. Meanwhile, the traveling electrons generate heat. The generated heat is proportional to the square of the current (Joule's Law). Heat induces resistance and resistivity of the nanostructures increased with the following relationship.

$$\rho = \rho_0 [1 + \alpha(T - T_0)] \text{ ----- (Equ.8)}$$

where ρ_0 is the initial resistivity, α is temperature coefficient of resistance, T and T_0 are the changed and initial temperature. Finally, as the electrical potential increases, the resistance is increased.

5.3. Interface

Our experiments have shown that the contact-sliding with an AFM can generate well defined microstructures. The friction force did not damage the line during sweeping. Instead, we observed that friction firstly continued the dots of debris/asperities (Figure

4.6a), then the line (Figure 4.6d), and finally completed it (Figure 4.7). The interface of Au and Si is strong enough to sustain sliding impact in later steps. Our results have shown that the thickness and width of the line range from a few atomic layers to a few microns. There are two potential explanations in the formation of the Au-Si interface: abrupt interface [157, 158], and an intermixed layer [159, 160]. Our results indicated that diffusion is likely to be a major factor since the line is found stable after forming. The frictional heating promoted the formation of the line. The formation of a stoichiometric compound is likely to bond the line onto the Si substrate. If a stoichiometric compound is formed, then the interface layer should be at least a few (tens of) atomic layers. If there is no such compound formed, the Au and Si will be bonded through Van der Waals forces. As shown in a TEM cross sectional image of a nanostructure (Figure 5.3), there are several layers. Each layer is randomly composed of the both materials of Au and Si. Those layers stacked up to at least 10nm in height. It is believed that the interface of the Au-Si system follows the intermixed-layer theory rather than the abrupt interface theory. This means that the gold atoms diffuse into the Si substrate so that they are mixed together under 2 atomic layers from the Si surface. When the mixed layers form at least 2 monolayers, the nanostructures start to grow on the surface by diffusion of Au and Si atoms, so called inter-diffusing. The atomic structure is the randomly mixed formation.

Thermal dynamically, the Au-Si follows the equilibrium phase diagram (Figure 3.4). Our results showed that the concentration of Au and Si are 27.29% and 72.71% as shown in the EDX results in Figure 5.2. at room temperature. Within a small length scale, a certain amount of Au is needed to relax the interfacial energy through surface interactions. The intermixed interfacial region between Au and Si materials reduces the interfacial free

energy to be stable. This is further proven by our nanoscale characterization in electrical conductivity. The microstructure apparently has much higher conductivity than that of silicon.

Our results demonstrated that the nucleation of the Au-Si interface as well as further crystallization forming the line were thermodynamically possible. Figure 5.3 shows particle-like features at the interfacial regions. This might be the nuclei. The diameter of such nuclei is around 2nm. The inside center is a grayish colored, but the outer is darker. It means that Au is a hypothetical nucleus for nucleation. If the gold nucleus is too small as a nucleus, the interfacial free energy would not be enough to induce nucleation. It's known that the critical nucleus radius (r^*) is following Equ.9.

$$r^* = -2\sigma / \Delta G_v \quad \text{-----} \quad \text{(Equ.9)}$$

where σ is surface tension and ΔG_v is the change per unit volume of the interfacial free energy. According to the equation, a nucleus bigger than a certain critical radius is needed to initiate nucleation. The critical radius depends on the surface tension and interfacial energy to be released. However, in the case of the contact-sliding, the frictional energy and the heated surface provide the interfacial energy, even though the gold nucleus is small ($r=0.5\text{nm}$). Hence, the equation (Equ.9) could be modified with the friction energy (G_F) and heating energy (G_T) as shown in Eqs.10 and 11

$$r^* = -2\sigma / (\Delta G'_v) \quad \text{-----} \quad \text{(Equ.10)}$$

$$\Delta G'_v = \Delta G_v + \Delta G_F + \Delta G_T \quad \text{-----} \quad \text{(Equ.11)}$$

Based on our results, we come to the conclusion that the nucleation and growth were most likely associated with the interfacial free energy through sliding friction stimulation. The interfacial free energy is released by forming stable nuclei, and eventually thermal

activation provides enough energy for intermixed-layers to be grown on Si surface. During the process, the both materials of Si and Au are inter-diffusing. Finally, the nanostructures are a randomly intermixed, a low-packing, and a non-equilibrium system.

6. APPLICATIONS*

In the research, the SPAN process was successfully demonstrated to fabricate nanostructures. The nanostructures fabricated by the process are applicable to be used in a biosensor and a flexible device. In this section, the feasibility of the nanostructures to be used in those applications is demonstrated. Sections 6.1 and 6.2 discuss about the applications as a biosensor and a flexible device, respectively.

6.1. Biosensor

The nanowires can be used as biosensors. One of the advantages of nanowire biosensors is that single-molecule can be detected due to their sensitivity and selectivity [120]. The applications of nanowires in biological areas are sought after in the present research. If the nanowires are fabricated on a piezoelectric material, the nanowires could be operated with the electrical power supply of the piezoelectric material. The piezoelectric material could be used as a sensor. Piezoelectric materials have been used as smart sensors [161]. To show the possibility of the SPAN application on a biosensor, the nanowires will be fabricated on a piezoelectric material, and then the piezoelectric material will be attached to cockroaches to detect their movement and generate an electrical potential. Piezoelectric materials have been used as smart sensors [162, 163].

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Polyvinylidene fluoride [PVDF, $(CH_2 - CF_2)_n$] has been widely used for research and in industrial applications [164-167]. PVDF is known for its durability, biocompatibility, and flexibility [167]. It is sensitive and can generate measurable voltages when subjected to a small mechanical force [168-173]. For this reason, PVDF has been used in various sensors, such as transducers, pressure sensors, acoustic components, and airborne sensors [164]. In this work, we developed a PVDF-based sensor and attached it to the limbs of cockroaches in order to monitor the movement of roaches. We work with a Discoid or False Dead-head roach, *Blaberus discoidalis*, a large-sized neotropical cockroach. The species are commonly distributed from Mexico to South America and is easily reared under laboratory conditions. Typically, roaches are found almost everywhere there is food and moisture [174, 175]. Using our approach, we found that the output voltage was a function of the degree of sensor bending caused by the movement of leg sclerites.

A PVDF thin sheet (from Measurement Specialties Inc.) was used to fabricate the sensors for our experiments. The PVDF sheet has a thickness of 52 μ m. A gold layer was deposited on both sides of the PVDF sheet using a hummer sputtering system (Anatech Corp.). The sputtering rate was set at 3mA for 10 minutes. The final thickness of the gold layer was 100nm (Figure 6.1a) for each side. After coating, the gold-coated PVDF was cut into the size as shown in Figure 6.1b. The sensor had a base of 2 \times 3mm. Thin wires (ANACONDA WIRE) of 0.3mm diameter were attached to both sides of the sensor using epoxy. Under the influence of bending, an electrical voltage is generated due to the piezoelectricity associated with PVDF. To measure and record the output voltage, the wires were connected to a real-time oscilloscope (Tektronix THS720P).

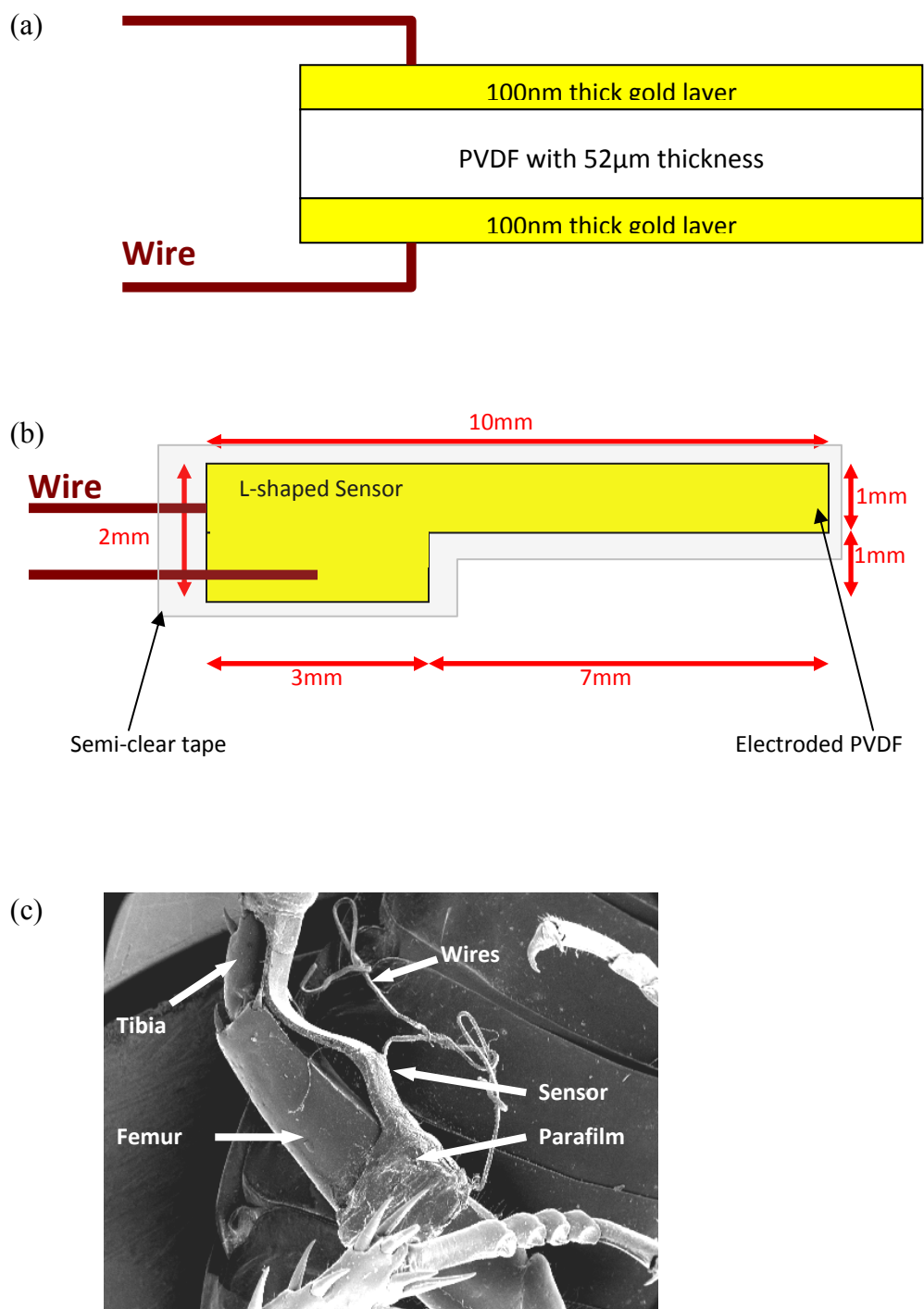


Figure 6.1. (a) Cross-sectional view for the gold-coated PVDF (b) L-shaped sensor used in the tests (c) Attachment location at the 'femur- tibia' joint is shown. The sensor was always placed on the inside area of the joint causing bending.

Sensors were attached to either male or female *Blaberus discoidalis* roaches that were about 50mm in length. The insects were firstly narcotized with CO₂. To attach the sensor, a commercial parafilm (PECHINEY ‘M’ Laboratory Film) was cut into 30 × 1 mm² strips. The strips were wrapped around the insect leg with the sensor placed on the inner surface of the leg (Figure 6.1c). Sensors were attached to the leg segment at the joint between the coxa and femur (CF), or femur and tibiae (FT). The sensors were placed on the inner surface of the joint as shown in Figure 6.1c. The metathoracic legs were studied instead of the prothoracic or mesothoracic legs, due to the greater degree of bending of the joints of these legs.

A live roach is hung from a cork that is fixed to a stand. The roach’s legs can move freely in this configuration. The roach grabs a foam ball (6.16g in weight) with its hanging legs (Figure 6.2). This setup is less complex than treadmills that have been reportedly used to study roach walking [176, 177]. The roach can move freely in any direction it may choose. Discoid roaches can drag 7.6 times (28.12g) their weight (3.7g) [178, 179] and can efficiently lift up to two times (7.4g) their body weight [178]. Two types of tests were performed. One was a “pulling test” where the foam ball was pulled away from the roach causing full leg extension. The second test was to make the roaches walk and run.

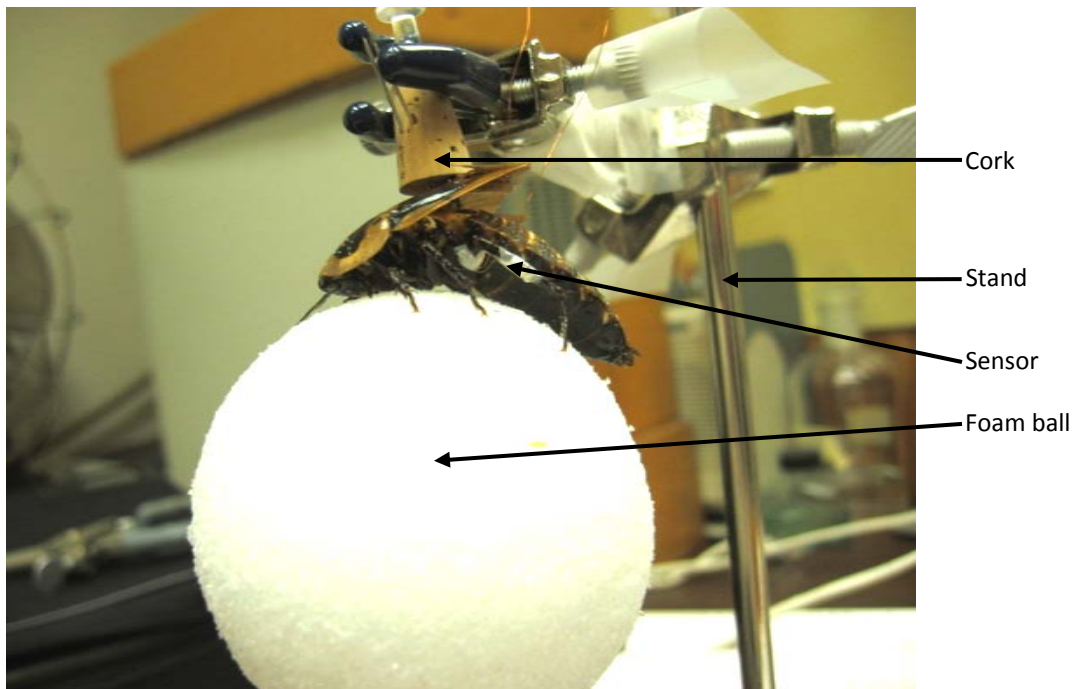


Figure 6.2. Experimental setup for roach (*Blaberus discoidalis*) tests. The roaches are suspended as they grasp and hold a foam ball. Sensor is already attached to the leg of the roach.

In pulling tests, the ball was pulled away from the roach making their legs stretch, along with the sensor. The legs flexed back when the ball was pulled beyond reach. In the walking/running tests, the ball rotated as the roach moved, and the sensor bent along with the leg. The degree of flexing in this case was less than in the pulling test. In addition, during walking/running, the sensor bent multi-directionally. The charge of the sensor was believed to be due the dipole alignment [180-183], and was sensed by the oscilloscope, which read out the resulting voltage.

Figure 6.3 shows a sample output signal recorded by the oscilloscope in real time during one of the pulling tests. The y-axis and the x-axis represent voltage of the output signal and time, respectively. Each segment along the y-axis represents 100mV, while

each horizontal segment corresponds to 1 second. In the Figure 6.3, the output signal is about 400mV. There were 26 sensors tested. Each test was repeated 20~50 times. The output signals varied from 20mV to 1000mV. During the pulling tests, the stretched legs straightened the sensor and then released it to the nominal position again.

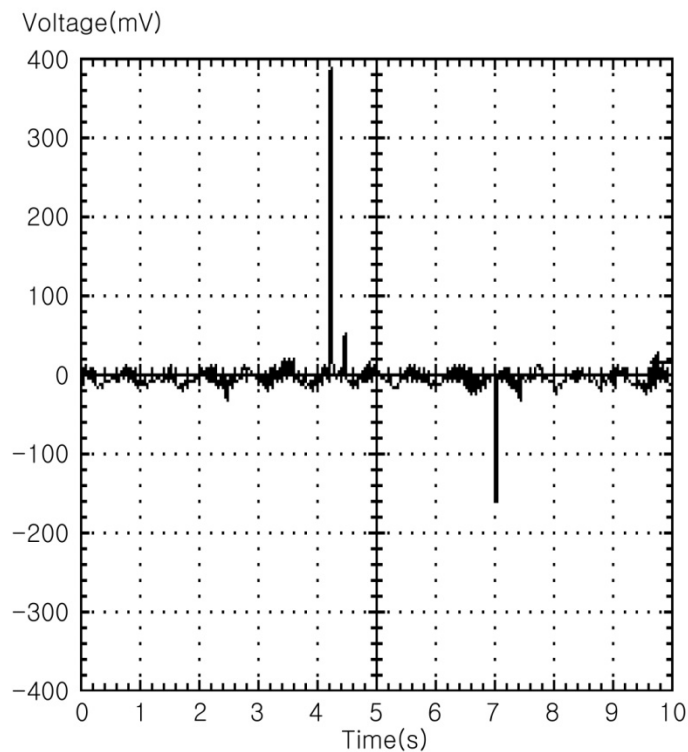


Figure 6.3. Pulling data image; the output signal is 400mV. Each vertical segment represents a voltage of 100mV, and each horizontal segment represents 1 second.

Our setup enabled the tethered roach to walk on the foam ball. Figure 6.4 shows the output signal from one of the walking tests, as recorded by the oscilloscope. In the figure, the x and y-axes correspond to time and voltage, respectively. The scales for x and y-axes of the figure were the same as for Figure 6.3. The output signal in Figure 6.4 is 200mV.

The same sensor was used for both pulling and walking tests. Even though pulling data is considered artificial, the walking data is considered natural since no additional external forces were applied to the foam ball. The walking results indicate that roaches could force the sensor to bend and to produce measurable voltage signals.

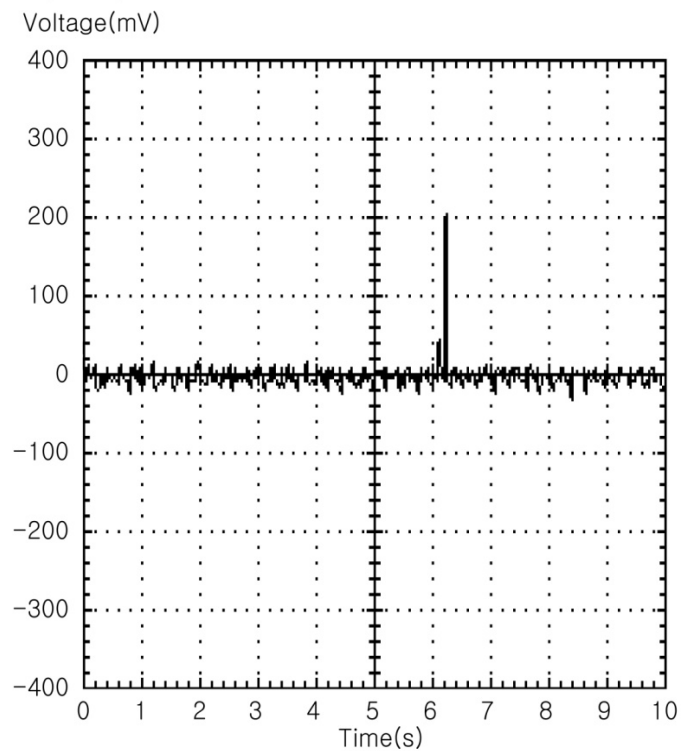


Figure 6.4. Walking data image; the output voltage is 200mV. Each vertical segment represents a voltage of 100mV, and each horizontal segment represents 1 second.

As noted in the experiments section, each sensor was placed across one of two locations: coxa - femur (CF) and femur - tibia' (FT). We are interested in determining if the attachment locations would affect the output signal. Indeed, the sensors attached on the CF generated a larger output signal than the FT attachment. One of the reasons was

due to the different bending angle of the CF and FT configurations. More details will be discussed in the following sections.

The generated signals from the sensors was conjectured to depend on the bending speed, angle, pressure, and force applied to the sensors [184, 185]. In the present work, the roaches bent and stretched their legs usually twice per second. As a result, there were two peaks per second on average (2Hz). When the roaches moved at a frequency faster than 2Hz, the output signal frequency was increased as a function of the roach's rate of flexing its legs, as shown in Figure 6.5(a). At a low rate of leg flexion, i.e. less than 2Hz, as shown in Figure 6.5(b), the number of the peaks are found to decrease. It was observed that the amplitude of the peaks was not related to the frequency.

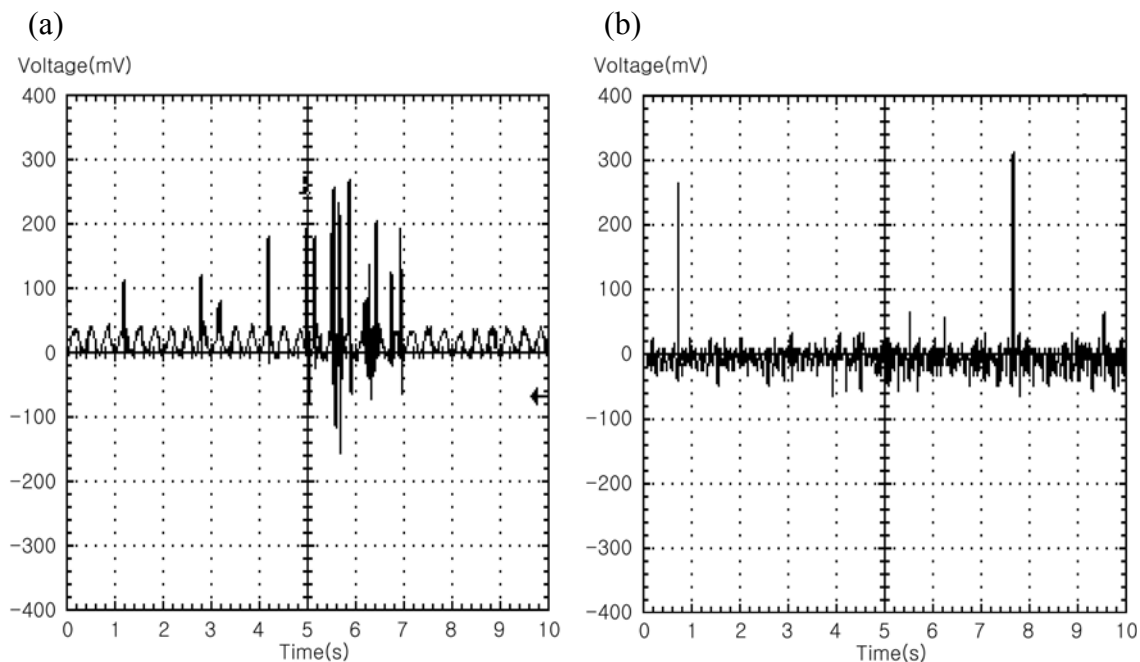


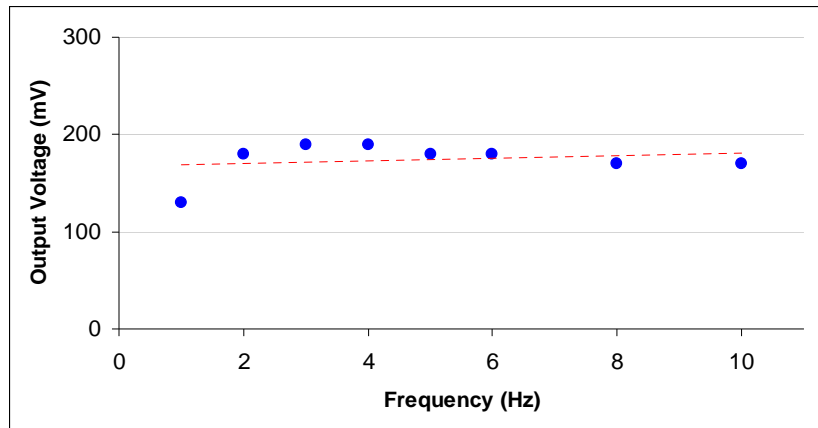
Figure 6.5. Voltage output generated by sensors attached to a roach's leg (FT joint) (a) running (b) walking.

The number of peaks is proportional to the bending frequency, but the voltage magnitude is largely independent of bending frequency. This proves that the Au-coated PVDF sensors can detect the speed of the roach movement; the higher the speed of the roach movement, the more frequent the generated voltage pulses.

The sensors attached to the CF joint generate higher amplitude signals than the ones attached to the FT joint. There are two possible explanations for this. One is due to the fact that the angle of CF movement was greater than that of FT; the other is due to the induced mechanical force of the CF joint. It was found that the CF joint angle ranges from 0° to 180° , and the angle of the FT ranges from 90° to 180° , when roaches move. Thus, the output signals from the CF configuration were higher than that from the FT configuration.

These results are believed to be due to the higher angle of the CF movement. In other words, the large magnitude of the voltage obtained in the CF configuration is due to the flexing of the joint over a large angle, but with the same speed, compared to the FT configuration. This shows that the sensors voltage amplitude is proportional to the angle of the joint flexion. The results were further supported by an experimental setup using a motorized linear stage. The motorized linear stage was designed to mimic the bending motion of the sensors attached on the roaches' legs with better repeatability. This system allowed us to control the angle (Figure 6.6a) and frequency (Figure 6.6b) of the bending sensors. As shown in Figure 6.6a, the amplitude of the output voltage was proportional to the bending angle (for a constant bending frequency). However, the amplitude of output voltage was largely independent of the bending frequency (for a constant bending angle).

(a)



(b)

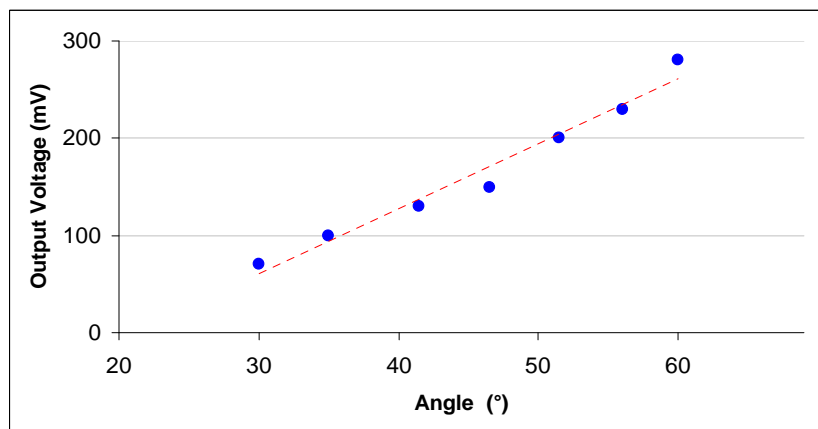


Figure 6.6. The linear motorized system mimics the bending motion of the roaches' legs. The amplitude of the output voltage was affected by bending angle (b), not by frequency (a). For bending tests (b), the frequency was fixed at 1Hz. The frequency tests (a) were conducted with a maximum bending angle of 97°.

In the present study, the sensors tested for CF attachment were also used for the FT attachment. The environmental temperature for all tests was kept at room temperature (25 ± 2 °C). The reason for keeping the temperature constant is that PVDF has pyroelectric

properties, which results in a change in the induced-output voltage with changing temperature [171, 186]. Under these conditions, the CF results could be directly compared to FT results. Figure 6.7 shows that the output voltages induced by the pulling tests are higher than for the walking tests (for both the CF and FT configurations). The voltages induced during pulling or walking are both significantly higher than when there is no movement at all. During the pulling tests, the roach legs were highly stretched, and after releasing the foam ball, the leg joints were folded at high speed. Under such high angles of bending, the output voltages from the pulling tests are seen to be higher than those for the walking test (Figure 6.7).

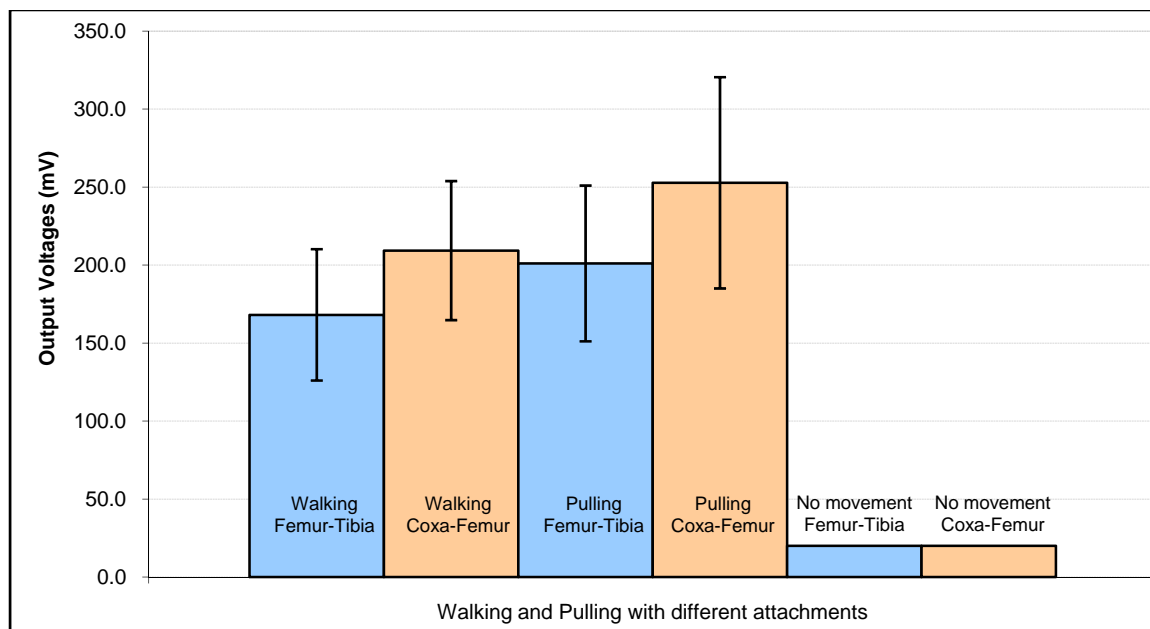


Figure 6.7. The output voltages of the experimental sensors under six conditions: Walking, Pulling and No movements, for two roach leg joints FT and CF using Discoid Roaches (*Blaberus discoidalis*).

The higher the bending angle, the higher the output voltage. Moreover, the voltages induced during the CF test are higher values than those induced during the FT tests. Since the coxa is the stronger leg segment, it is responsible for the initial “pushing” which leads to locomotion [187]. The CF joint folded with higher angle than the FT joint, in the same time interval, hence yielding larger output voltages.

The development of a technique to use polymeric sensors to monitor a roach’s leg motion is presented here. With appropriate attachment locations, sensors were proven to be able to produce a measurable output voltage in tandem with the cockroach’s movement. Results from pulling experiments indicated that the gold-coated PVDF works as a functional sensor that monitors the roach’s leg movement. The signal data taken from roach walking on a foam ball proved that the legs of the roaches could bend the electrode coated PVDF sensors and repeatedly produce measurable output voltages. Comparing the two joints studied, it was found that the amplitude of the generated voltages from the sensors placed on the CF joint was higher than those placed on the FT joint due to the larger bending angle of the CF joint. In addition, the higher the speed of the roach, the more the frequency of the generated voltage pulses. This result was consistent with tests done with a motorized linear system. Our results further show that the amplitude of output signal was dominated by the bending angle. The output frequency depends on the bending frequency. Therefore, it can be concluded that PVDF based sensors can be used to measure the speed of roach movement, by means of the induced-voltage pulses from the PVDF sensor. Monitoring a roach’s movement could bring insights into the behavior of insects and environments. In the long run, such a study could lead to mimicking the physiological behavior of natural biological systems.

6.2. Flexible Device

As mentioned above, piezoelectric materials have been widely used for research and in industrial applications [188, 189]. Once the materials are physically stressed, they generate electrical potentials. Among those, there are flexible materials such as polyvinylidene fluoride [PVDF, $(CH_2 - CF_2)_n$]. If the nanowires are fabricated on these flexible piezoelectric materials, it will show the possibility that the flexible devices can be fabricated.

For Micro-electro-mechanical systems (MEMS) and Nano-electro-mechanical systems (NEMS), the piezoelectric materials play an important role [190, 191]. Polyvinylidene difluoride (PVDF) has been widely used in engineering applications due to its favorable chemical and mechanical properties [164-166, 192, 193]. The properties such as high piezoelectric coefficient, good flexibility, biocompatibility [194-196], low acoustic and mechanical impedance, and light, are especially unique for MEMS applications. The PVDF is one of the most widely used piezoelectric materials in the fluoropolymer family. Its piezoelectricity was discovered in 1969 [170]. However, the mechanism of piezoelectricity has not been clearly explained [197-199]. Its uncertainty obstructs the development of MEMS or NEMS. Many researchers have attempted to explain the origin of the piezoelectricity of the polymer [173, 200-203] and to investigate the structure change of PVDF due to electric field [203] and temperature [204].

Recently, development in new characterization techniques, particularly nanoscale analysis, has made it possible to bring new insights from the piezoelectricity measurements. One of the techniques is the Atomic Force Microscopy (AFM). It enables to characterize surface morphology at nano-scale. Using an AFM, we investigate the

relationship between the piezoelectric properties and structures of PVDF. An electrical field was applied to the PVDF samples. The effects of the external electrical voltage on the sample surface was observed using AFM. The responding time of the PVDF samples to mechanical stress was studied.

This study was performed using three different samples of polyvinylidene difluoride (PVDF). The original PVDF film samples were stretched to obtain the polar β -phase. The samples were examined for polarity (β -phase, TTT type) and piezoelectric coefficient (23pC/N). Using a metal evaporator, the metal coatings were deposited on the films as electrodes. Two samples of 110 μ m and one of 52 μ m of film thickness were coated with 28 μ m of Ag, 600 Å of NiCu, and 150nm of Au, respectively. The size of all samples was 3cm in length and 1cm width.

A home-built splitter, i.e., a Shark box, was attached to an atomic force microscope (AFM). The splitter distributes electrical potentials and passes a current from samples to a picoameter. The splitter was powered through the AFM's built-in power supply as shown in Figure 6.8. The sample surface is scanned with a standard Si₃N₄ probe. The surface profile is measured against the electrical potential. When bent, a PVDF sample generates a micro-ampere current flowing to a computer through a picoameter. The computer records the output current with LabView program. In this case, conductive AFM probes were used.

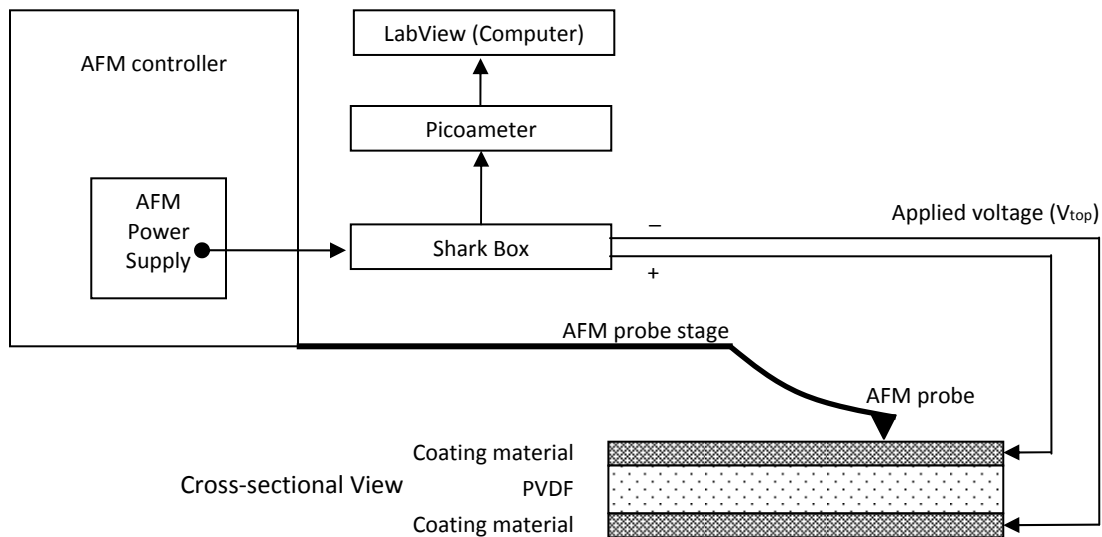


Figure 6.8. Diagram for the experimental setup and characterization. This work was done with an AFM Si₃N₄ probes. The external electrical potentials were supplied to the PVDF samples throughout the shark box which is a splitter for electrical potentials and current.

A motorized linear stage (MLS) was used to characterize the responding time of the dipoles in PVDF. The coated PVDF samples were placed on the MLS from end to end as shown in Figure 6.9a and b. With one of the sample holders fixed in a stationary position, the other holder moved reciprocally at a frequency of 4Hz in a stroke length of 3cm. This reciprocal motion buckled the PVDF samples. The buckling induces internal mechanical stresses. Figure 6.9c shows the AFM setup applying a force on one end. As a result, the PVDF sample produced an electrical potential due to the piezoelectric property. The voltage generated was recorded using the LabView throughout a picoameter.

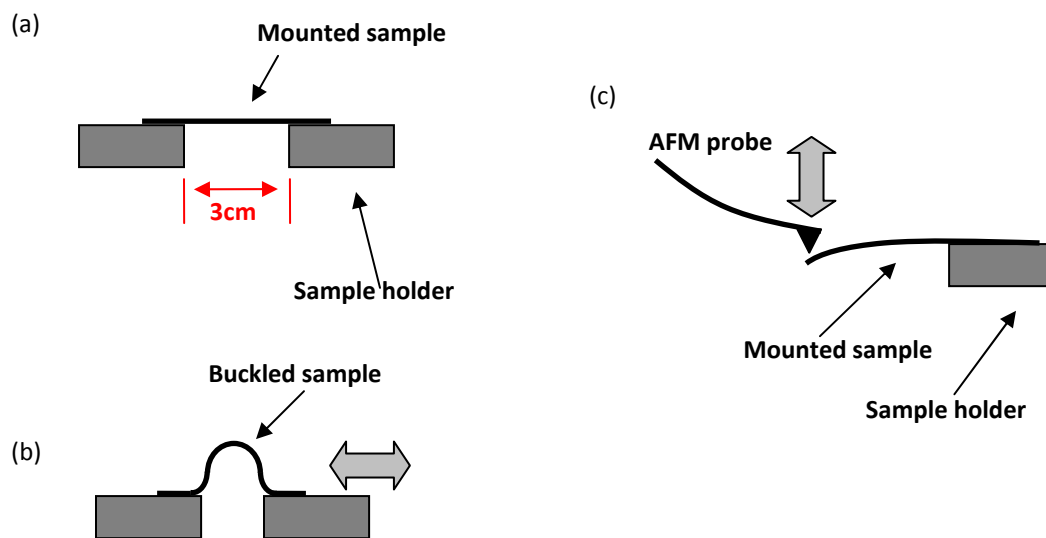


Figure 6.9. Simple diagrams of Motorized Linear Stage (MLS) (a, and b) and AFM setup (c) to apply mechanical stresses on a sample.

Scans of a PVDF sample, with the thickness of $110\mu\text{m}$ and an Ag-coating of $28\mu\text{m}$, were conducted using the AFM with a standard Si_3N_4 probe. Figure 6.10(a) shows a scanned image of the sample prior to connecting the Shark box. The same region of the sample was scanned with the leads of the shark box connected to the PVDF as shown in Figure 6.8 while an applied electrical potential was at 0V .

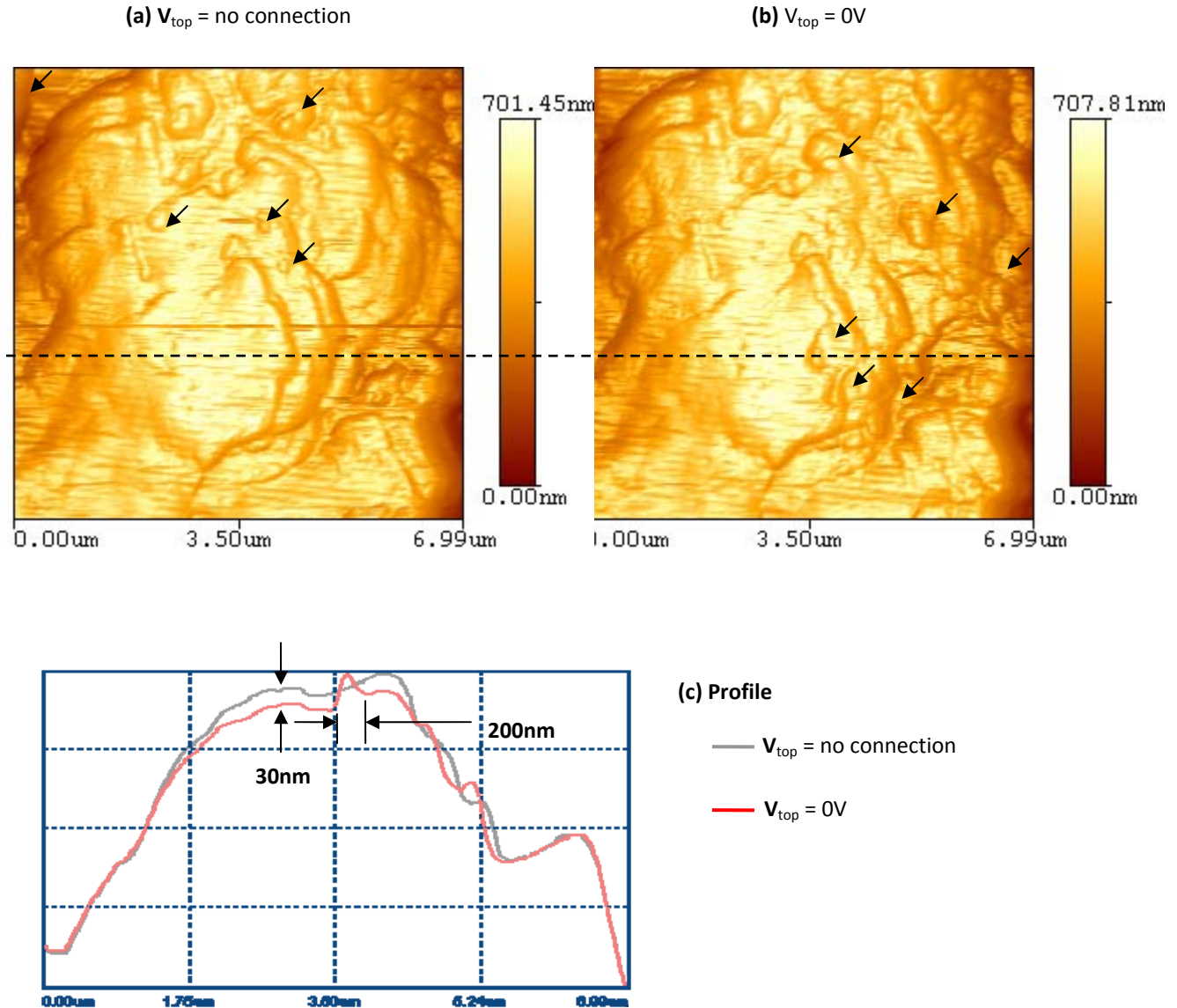


Figure 6.10. AFM images for the PVDF sample surface which was $110\mu\text{m}$ thick with $28\mu\text{m}$ Ag coated without (a) or with (b) connection of electric field. (c) The profiles of the two scans along the dashed line.

The profiles of the two scans along the dashed line are shown in Figure 6.10(c). It is seen that at the zero potential, the surface profile underwent a morphology change resulting in an increase in roughness.

As the electric potential on the PVDF is increased in discrete steps of 0, 4, 7, and 15V, the roughness (R_a) is seen to increase in a nearly linear manner as shown in Figure 6.11. A similar process was conducted for a PVDF sample of $110\mu\text{m}$ thickness and 600\AA NiCu coat to characterize its surface morphology with external electrical potentials of 0 and 5V. As shown in Figure 6.12, the surface texture was squeezed with the application of 5V. This is believed to be due to the dipole realignment resulting in an increased surface height. A PVDF sample with the $52\mu\text{m}$ thickness and a 150nm-Au-coating, was used to determine the amount of deformation caused by the applied voltage. This sample showed an original wavy surface when no voltage was applied.

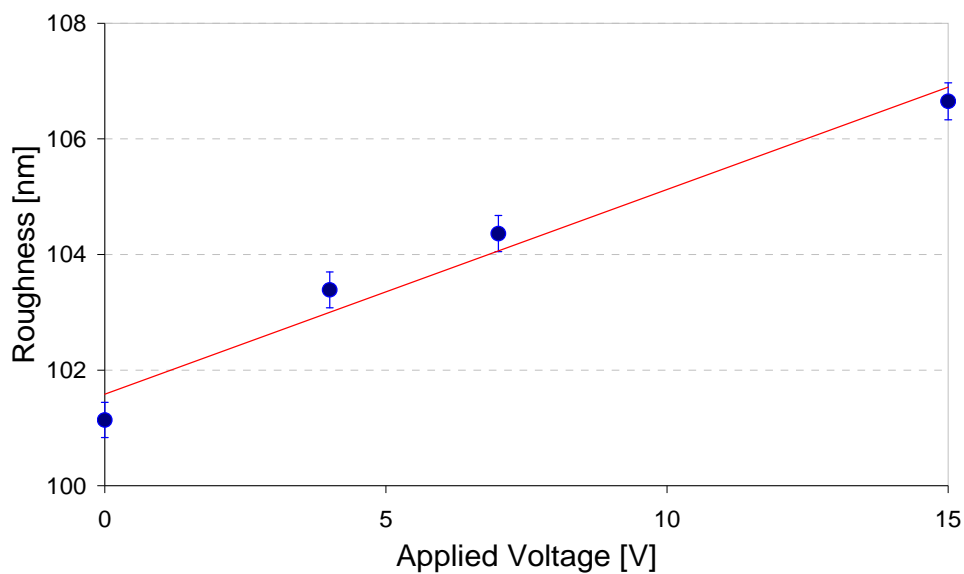


Figure 6.11. Roughness change with the applied voltage.

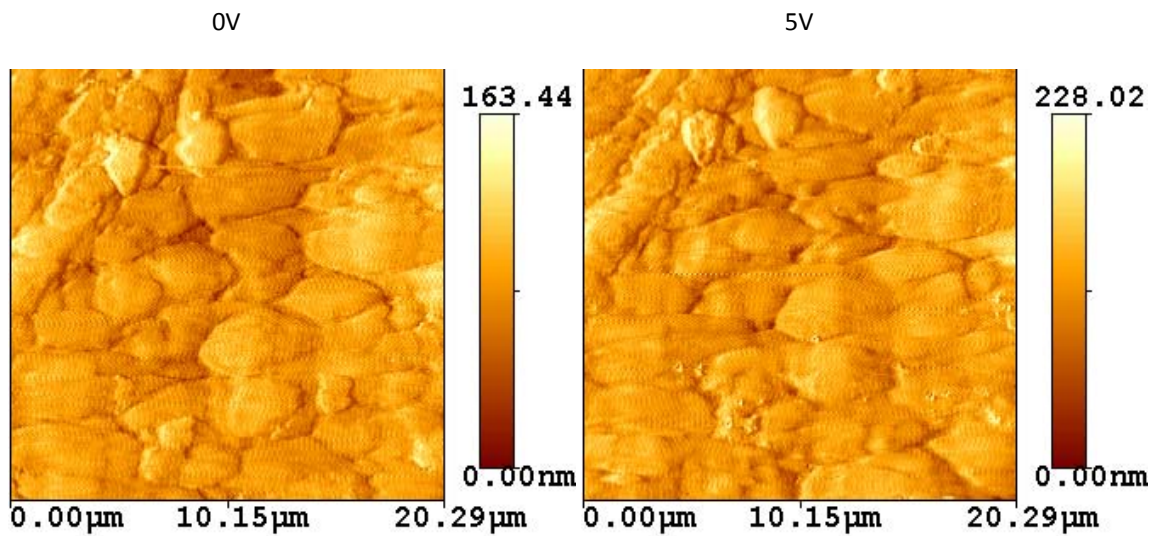


Figure 6.12. The texture change of a PVDF surface. The PVDF sample of $110\mu\text{m}$ thickness with 600\AA NiCu coated was characterized for its surface with external electrical potentials, 0V (a) and 5V (b).

As a voltage was increased, the amount of squeezing deformation was observed and calculated through measuring the peak-to-peak distance (D_{pp}) of the wave surface. Figure 6.13(a) shows AFM images of the sample under different voltages. Figure 6.13(b) displays the change of the D_{pp} due to external electrical potentials. As the applied electrical potential increased, the D_{pp} was decreased with the rate of 28.7nm/V . The slope of the gray line in the figure is equal to the effective piezoelectric coefficient. From the results of MSL test, the response time, t_r , is calculated as the difference between the initial moment when stress is applied, t_i , and the time when a voltage signal is produced by the sample, t_o , such that $t_r=t_o-t_i$. It is seen that there is a delay between the blue and dotted line of 0.155s which is equal to the response time (t_r).

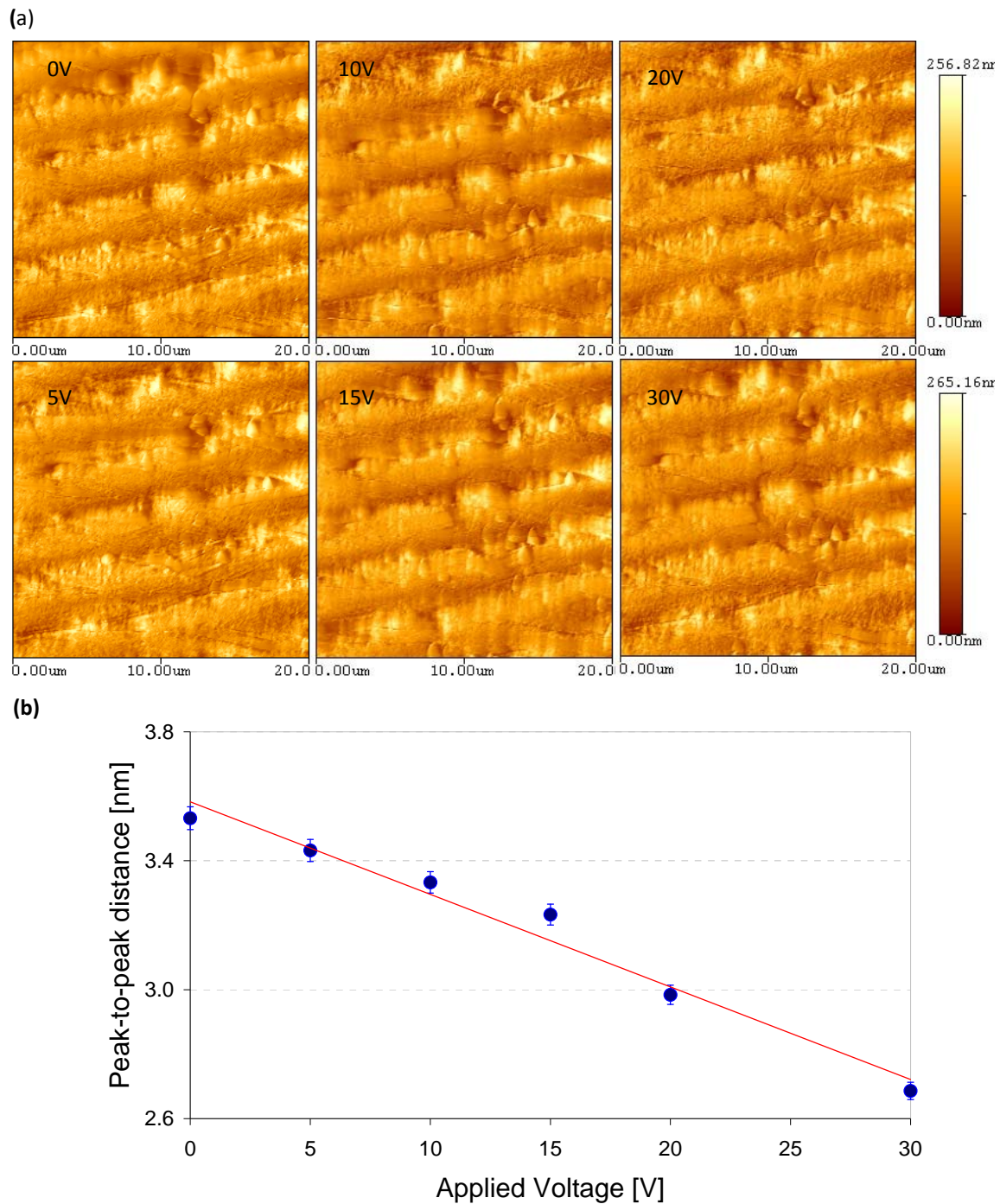


Figure 6.13. Investigation of the surface change rate with the applied voltage. (a) AFM images for a PVDF sample which was $52\mu\text{m}$ thickness with 150nm Au coated. (b) The change of the peak-to-peak distance (D_{pp}) with the external electrical potentials.

Some polymers have showed transformation on its surface due to external stimuli [205-207]. The surface of an electro-sensitive polymer changed polymer chains and alignment with an applied electrical field [208]. Figures 6.10, 6.12, and 6.13 demonstrate the effect of inverse piezoelectricity due to the alignment of the dipoles in response to an applied electric potential. As a result, the potential caused the change of a surface morphology. Furthermore, the alignment of the dipoles was a temporary response to a voltage and returned to their original state after the voltage was removed. This effect is caused by lamellae (usually 20nm thick) of crystalline embedded within amorphous regions (chain-folded model) of the samples. It was reported that one a 3D polymer was fixed at its ends, the local intra- and inter-chain associated orientational-deformational interactions could induce spontaneous ordering [209]. The lamellae were randomly oriented with no electric potentials present in the sample. As an electric potential was applied, the lamellae realigned to orient their dipole angle and moment to correspondingly. The higher the applied potential, the greater the realignment, and in such, the greater the peak-to-valley distance. The rate of the D_{pp} change was about 1nm per 2V.

In order to understand the piezoelectric behavior at different length scales, we compared the charge output under stresses at nanometer and millimeter scales. The first measurement was conducted by applying external mechanical stress on the samples with the MLS shown in Figure 6.9. As the distance between the sample holders decreases, a mechanical stress is induced into the sample resulting in the alignment of the lamellae which produces a voltage due to the alignment of the dipoles. Figure 6.14a shows the response of such a piezo sensor.

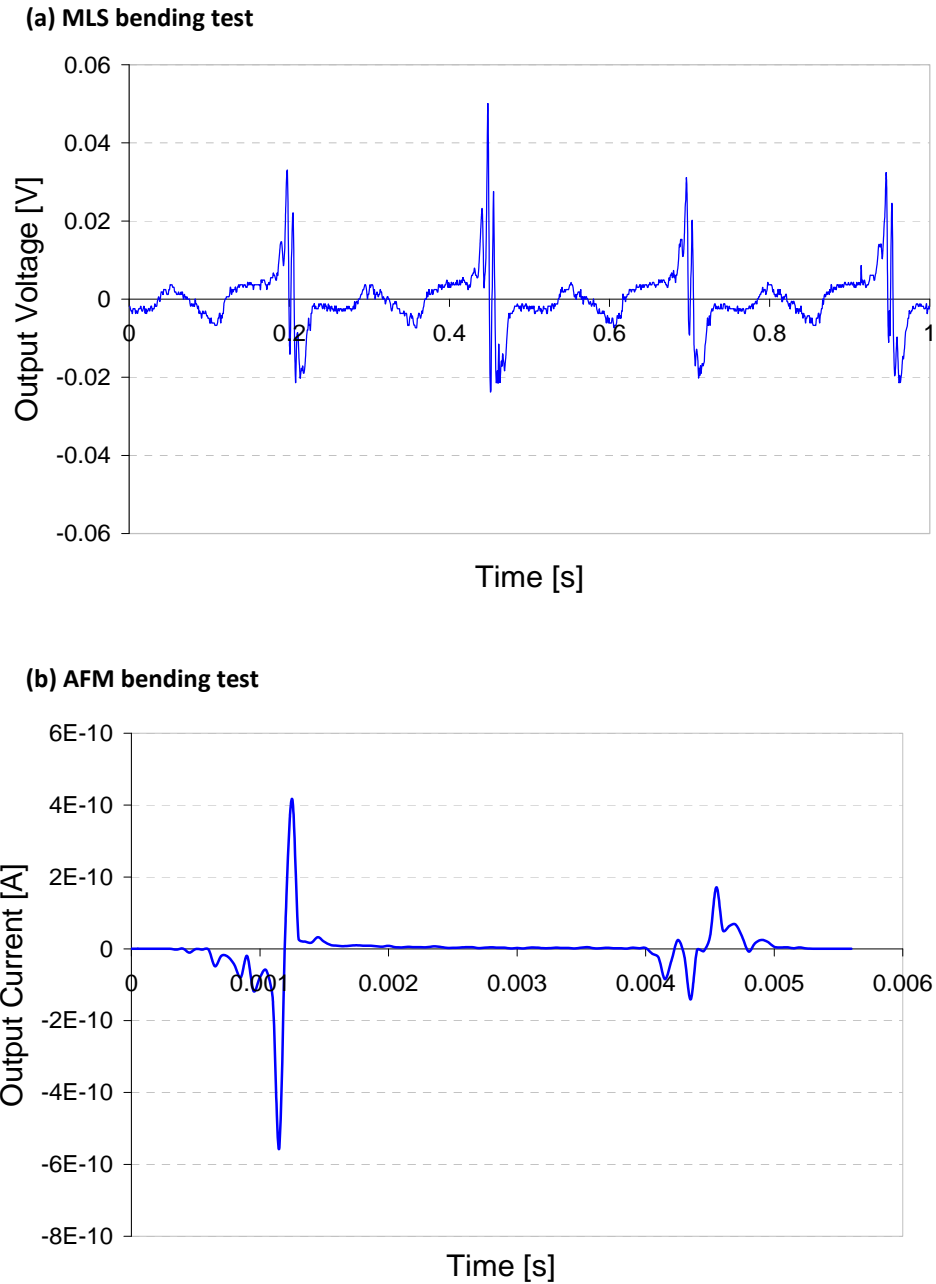


Figure 6.14. The results of (a) Motorized Linear Stage (MLS) and (b) AFM tests showing the output have similar behavior at different scales. The MLS test was to fix two ends of the sensor while the AFM was on a sensor that was fixed at one end only with manual bending.

The voltage produced by the PVDF is proportional to the amount of stress applied to the sample which is proportional to the distance traveled by the MLS holders. Since the sample holder is moving in a reciprocating motion, there should be no movement at its maximum and minimum deflection and not cause any further alignment of the dipoles resulting in a voltage output of 0V. The AFM was used to test the output of a piezo sensor when a down force was applied at one end of the polymer. Although the units of the MLS and AFM were different, the trend of their outputs was the same. This means that the localized dipole alignment is correlated with the global behavior. When an external force was applied, local or global, the PVDF produces a charge and subsequently a morphological change. The effects of electrical potential on microstructures and phase transformation of PVDF have been discussed elsewhere [210]. Since our focus of this research is on the surface morphology study of a piezo sensor, we will not discuss this aspect in details.

Metalized PVDF samples were characterized using an AFM and an external MLS in order to study the mechanisms of piezoelectric effects. Results showed that under an external potential, the surface roughness was increased. Under stress, an output was generated that is scale independent. Such variations are important for design consideration of MEMS devices and their sensitivity.

7. CONCLUSIONS

7.1. Summary

This research demonstrated that by using an atomic force microscope, we were able to generate well-defined micro- and nano- structures that have unique properties. We developed a new method to fabricate nanostructures. The process is called the scanning probe alloying nanolithography (SPAN), using an Au-coated AFM probe to slide against a Si substrate. The increased volume of those structures from the tip wear and their conductivity from the substrate indicate the nucleation and growth of potentially controllable nanostructures. The friction-stimulated structures are different from their original state (Au and Si). As a result, nanostructures (wires, dots, and logos) were fabricated. The properties of the nanopatterns were characterized using high resolution techniques. The line structures had about 10nm in height and 600nm in width. A dot formed by the process was 153nm high and 400nm wide. The electrical properties were also measured using AFM. When the electrical potential 12V was applied, current of $1.07 \times 10^{-3} \text{A}$ was recorded. At low electrical potentials (-0.3~0.3V), the measured resistance R_{low} was 5k Ω . However, the resistance was increased at high potentials ($R_{\text{high}}=14.3\text{k}\Omega$) due to electrons traveling and heating the material. The conductivity measurement reveals possible interactions between the AFM probe and the substrate material. The mechanisms of SPAN were discussed. The contact-sliding induced friction and thermal energy resulting in the temperature increase. The Au material was transferred from the probe to the Si substrate. Finally, the nanostructures were grown on the substrate.

Assisted by an external potential, the electrical adhesion was involved in the process. The dimension of the nanopatterns could be controlled with the power system. The SPAN technique is expected to be independent from materials and highly controllable. It would generate nanostructures precisely and effectively. Our experiments indicate that minimizing nanostructures is possible using a sharp AFM probe. With a sharpened tip and lower down-forces of a tip on a substrate, the lines can be further downsized.

7.2. Research Impacts

The SPAN will open a new avenue to fabricate nanowires and help to extend the Moore's prediction which cannot be achieved by the conventional photolithography. To improve the efficiency of SPAN, this research optimizes the process by understanding the scientific phenomena of material transformation, phase change, and nanostructure formation. This will also explain the fundamental principles involved in this process, such as nano-friction and nano-wear behaviors between the AFM probes and the substrates. In addition, this study will show the possibility of using the nanowires in biosensors and flexible devices.

7.3. Future Recommendations

There are some future recommendations to improve the research. During experiments, the down-forces of the probes against the sample surfaces couldn't be measured in real-time. Up to now, the forces were only calculated. To obtain more accurate data and precise control the probe, the real-time measurement for the probe down-forces is recommended.

Probes with different spring constant and sharpness are recommended to be used for the SPAN process. This will bring further understanding of the interfacial mechanisms and further optimization of the SPAN. It will also provide more opportunities for new applications.

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